

FIG. 1

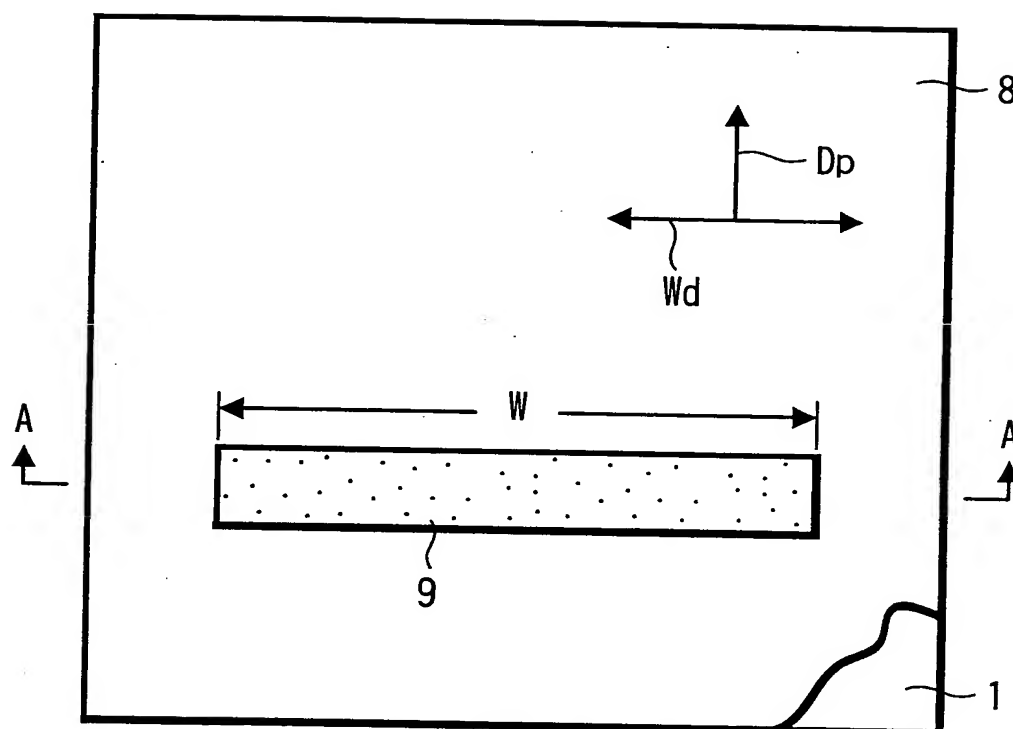
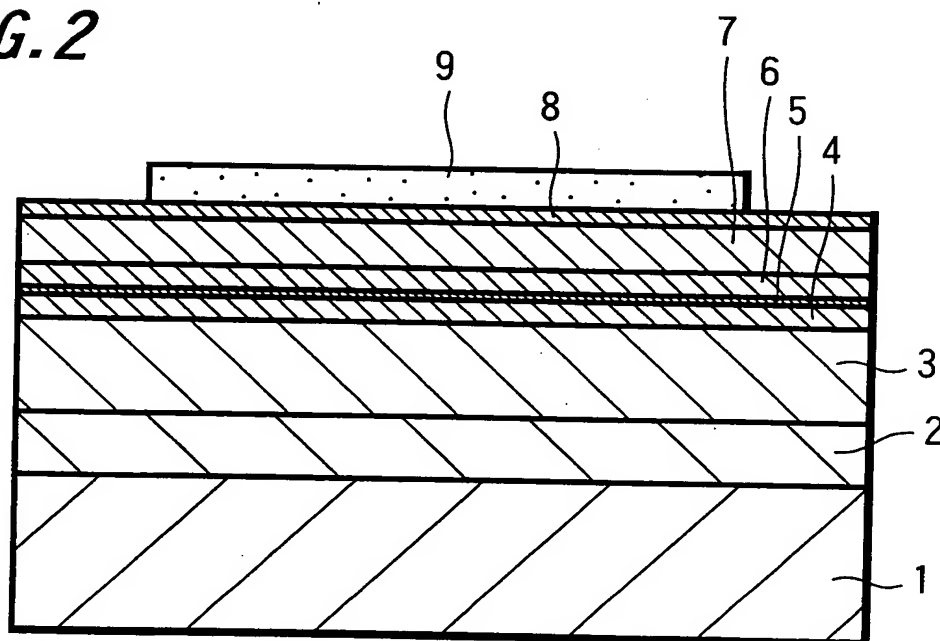


FIG. 2



A cross-sectional view of a semiconductor device. The device consists of a substrate (1) with three distinct layers (2, 3, 4) stacked vertically. Layer 1 is the bottom-most layer with diagonal hatching. Layer 2 is the middle layer with diagonal hatching. Layer 3 is the top layer of the substrate with diagonal hatching. On top of layer 3, there is a structure (4) with a flat top surface. This structure (4) is composed of several layers: a bottom layer (5) with diagonal hatching, a middle layer (6) with diagonal hatching, and a top layer (7) with diagonal hatching. The top surface of layer 7 is labeled S1. The side surfaces of the structure (4) are labeled G1. The top surface of the substrate (1) is labeled 9. The side surface of the substrate (1) is labeled 8.

[illegible]

FIG. 5

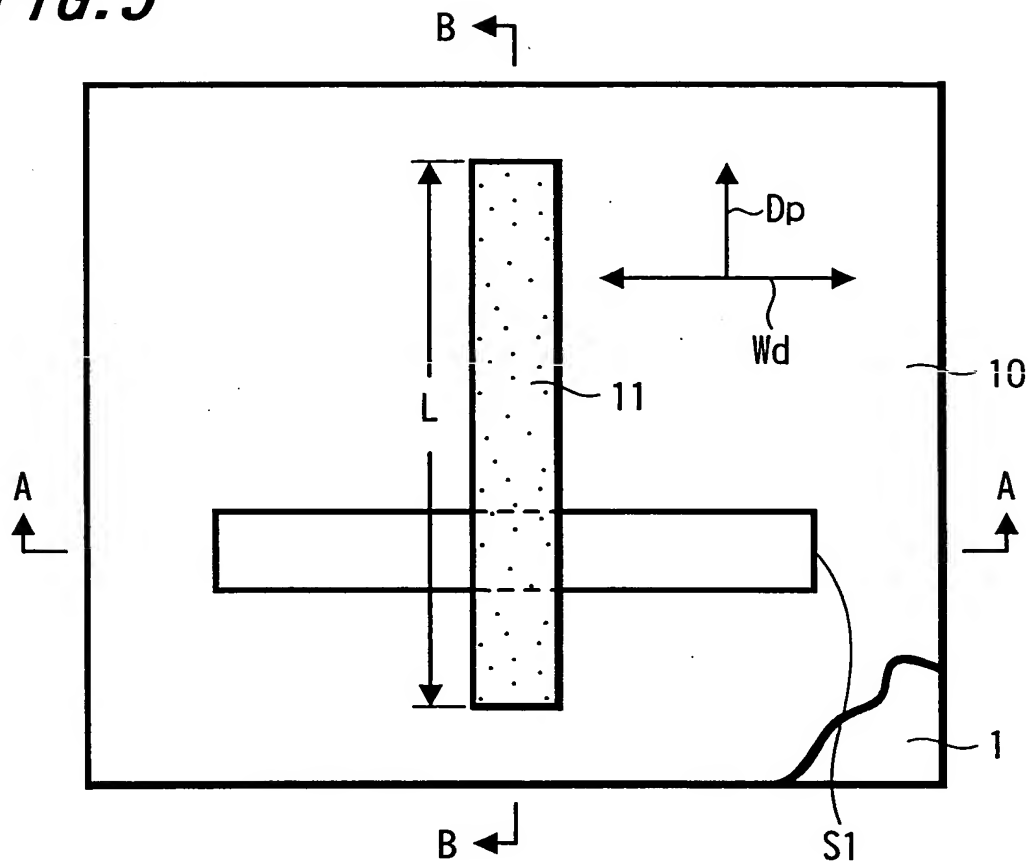


FIG. 6

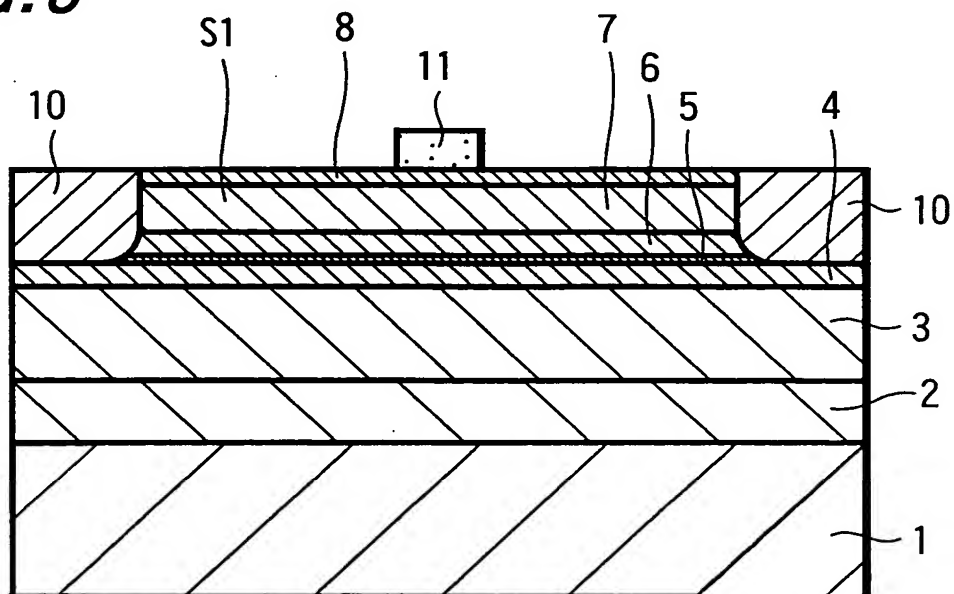


FIG. 7

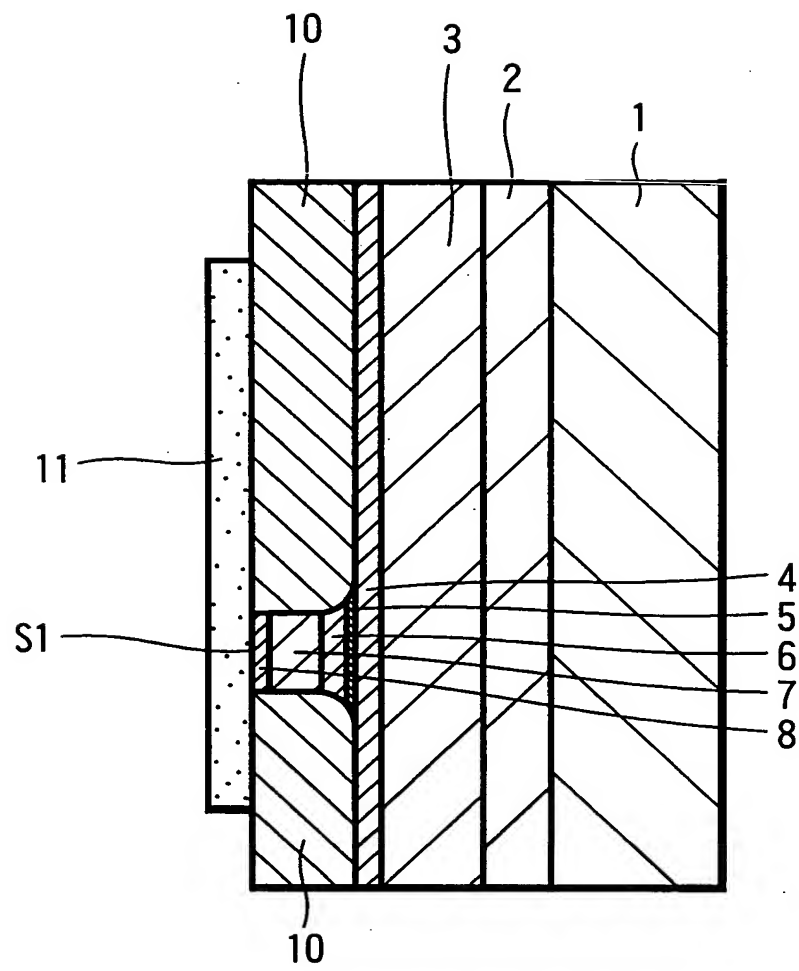


FIG. 8

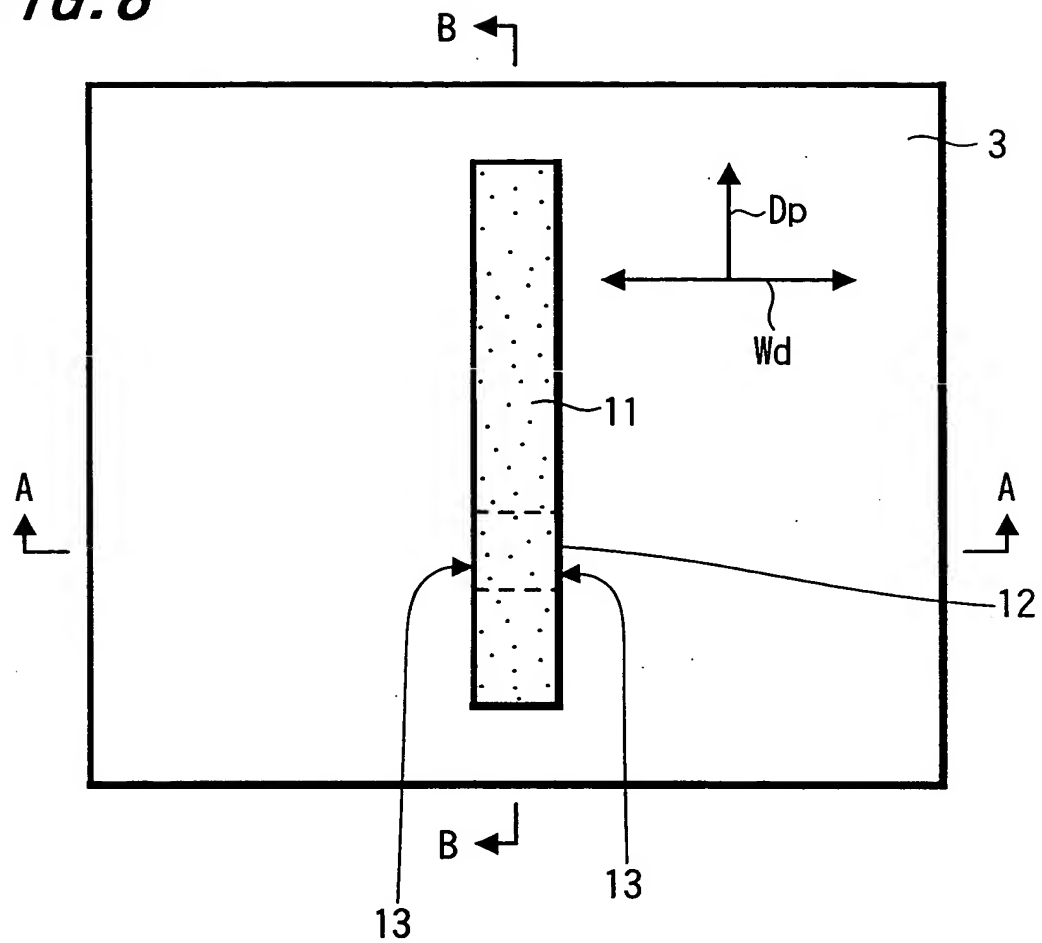


FIG. 9

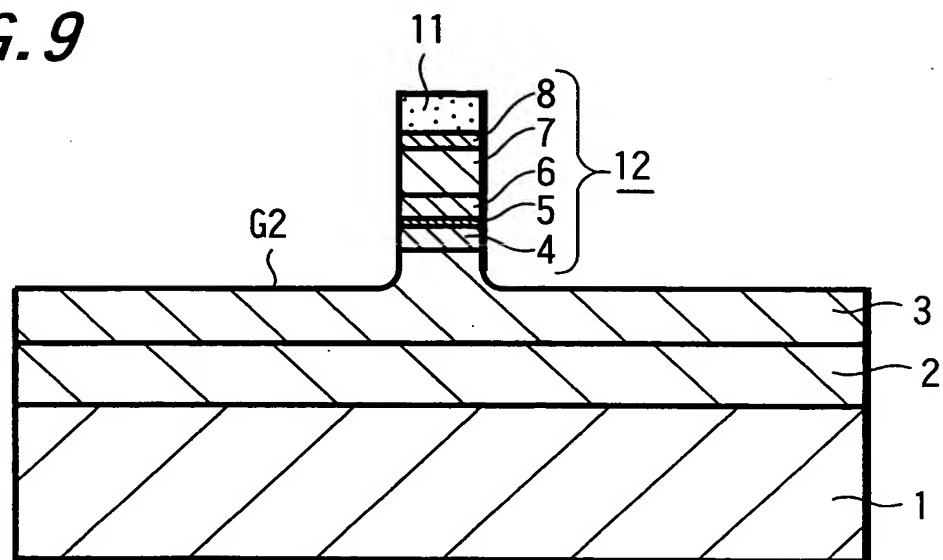


FIG. 10

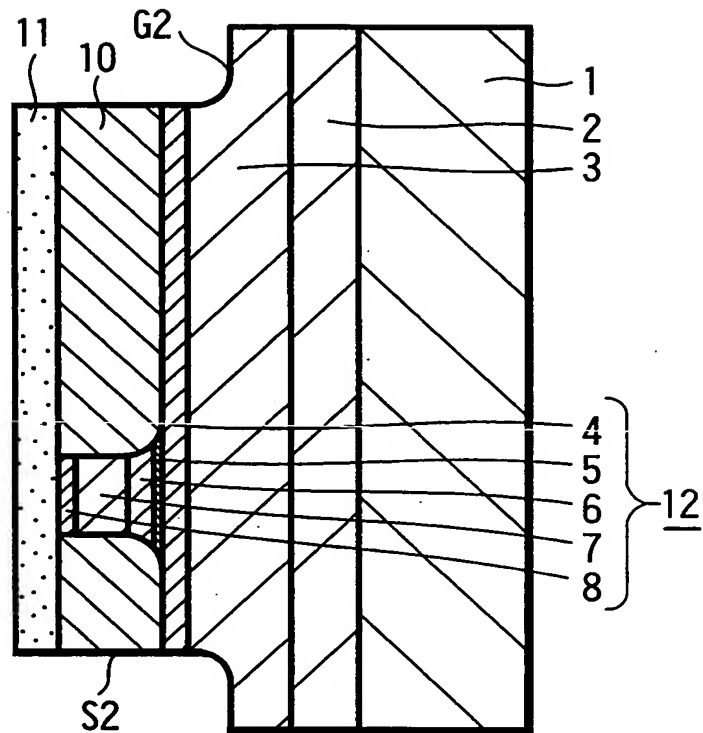


FIG. 11

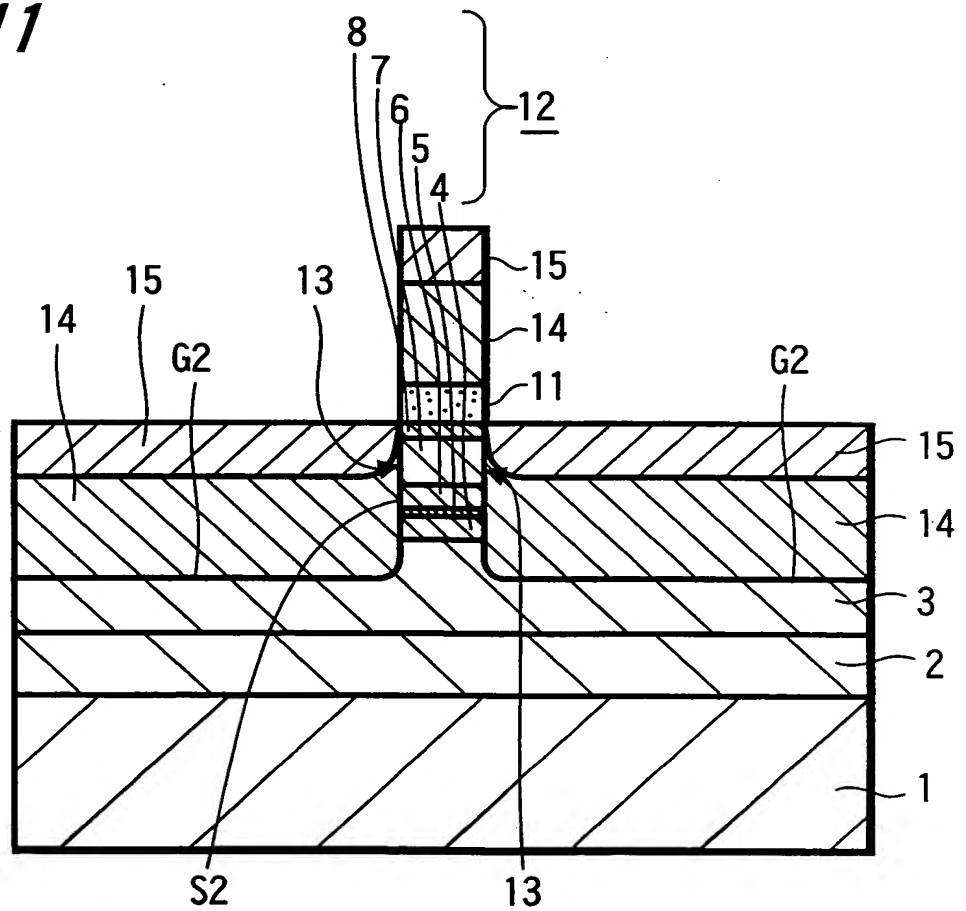


FIG. 12

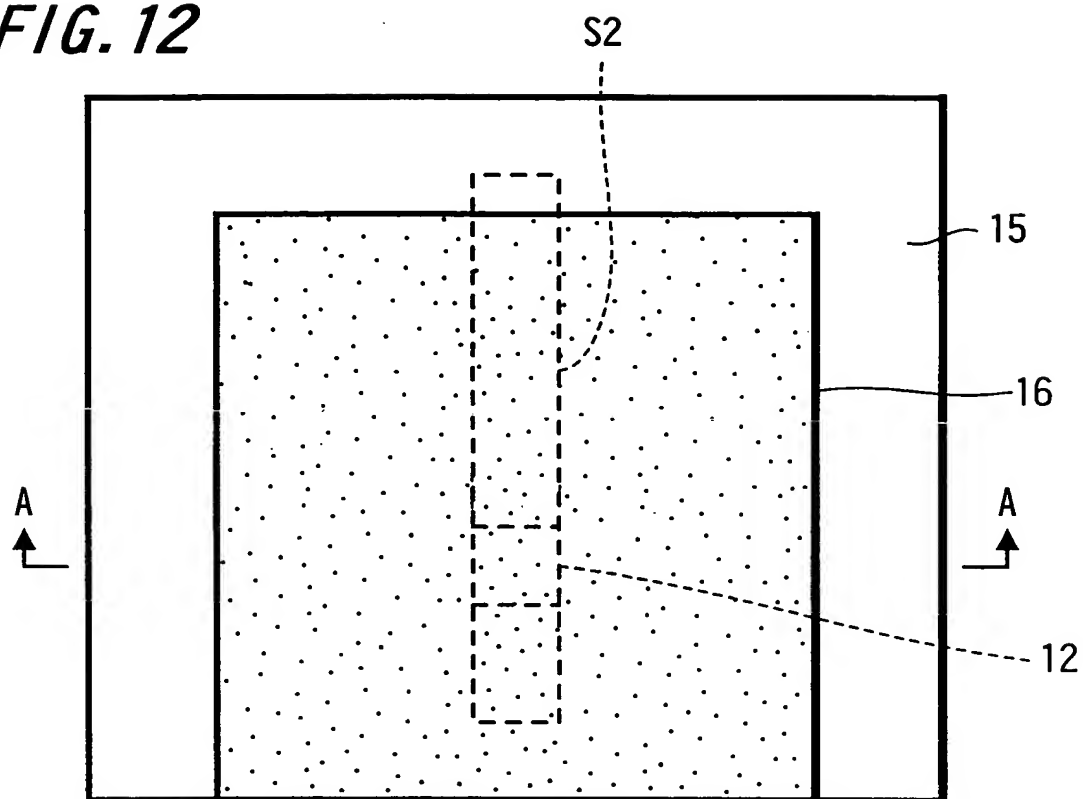


FIG. 13

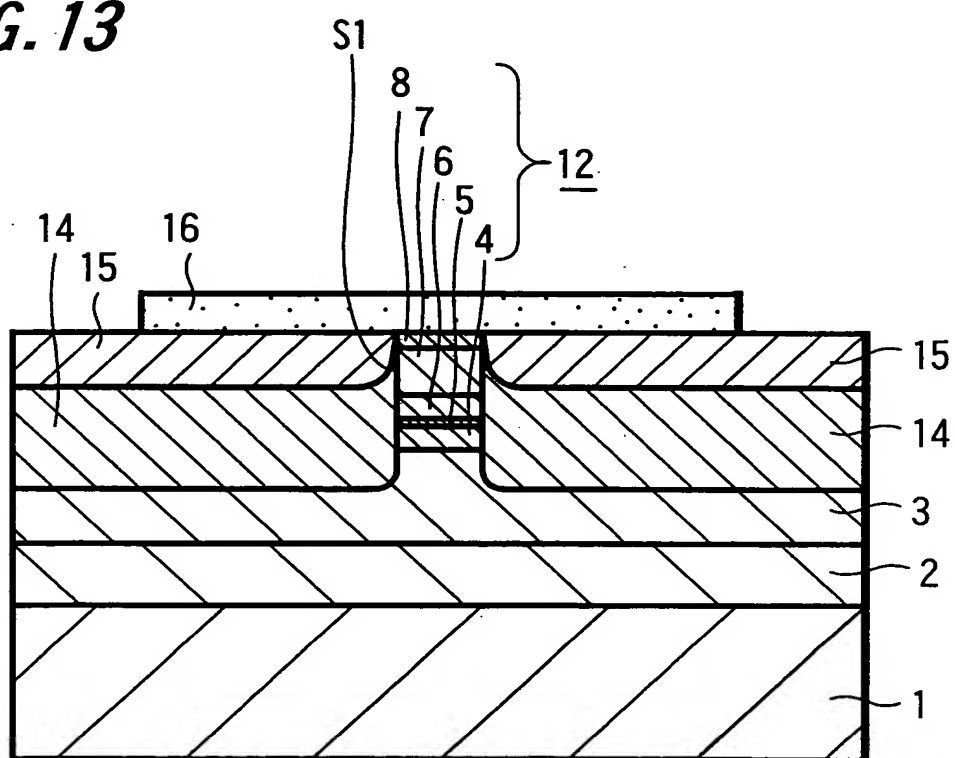


FIG. 18

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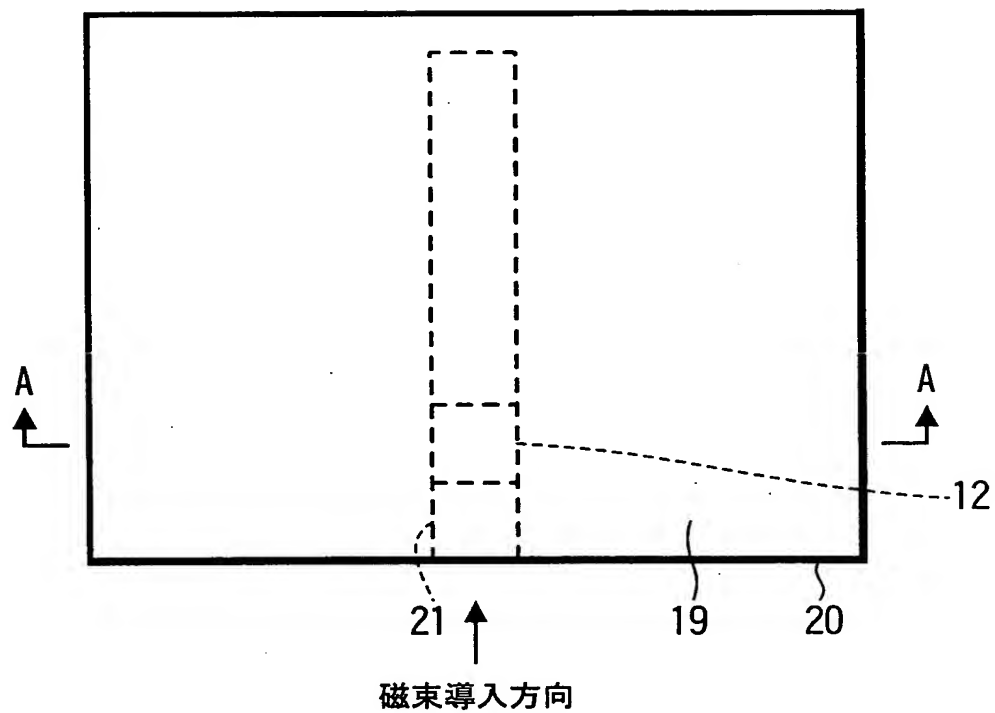


FIG. 19

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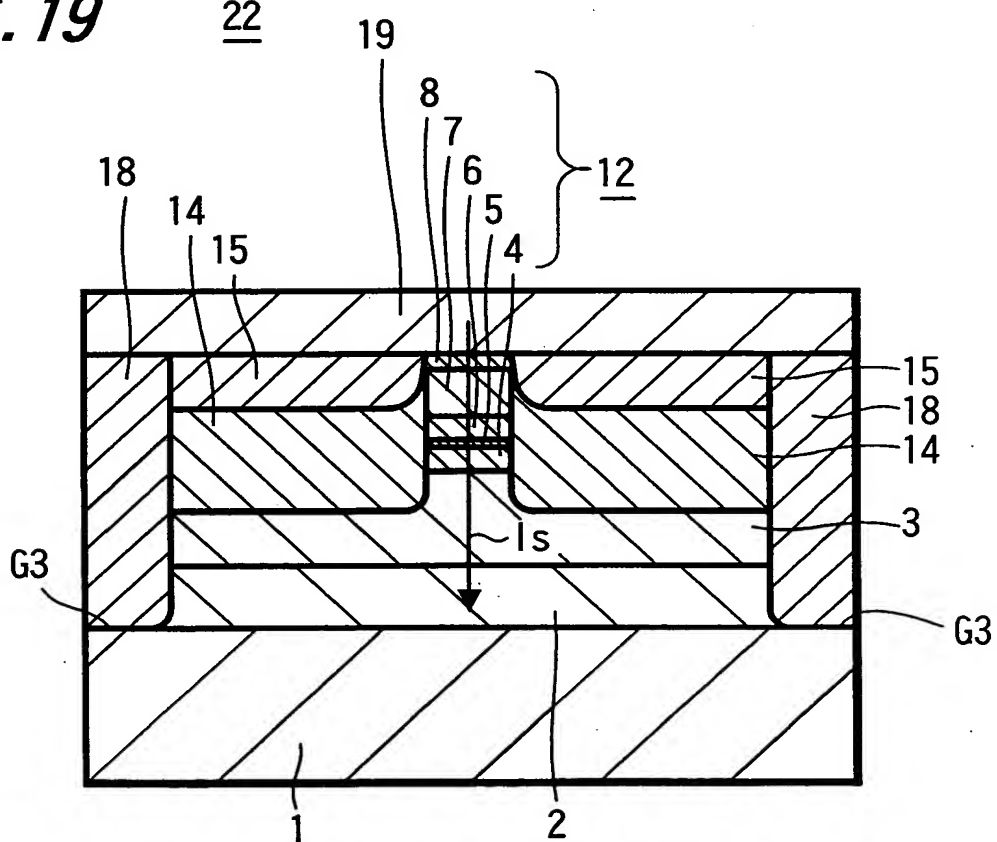


FIG. 20

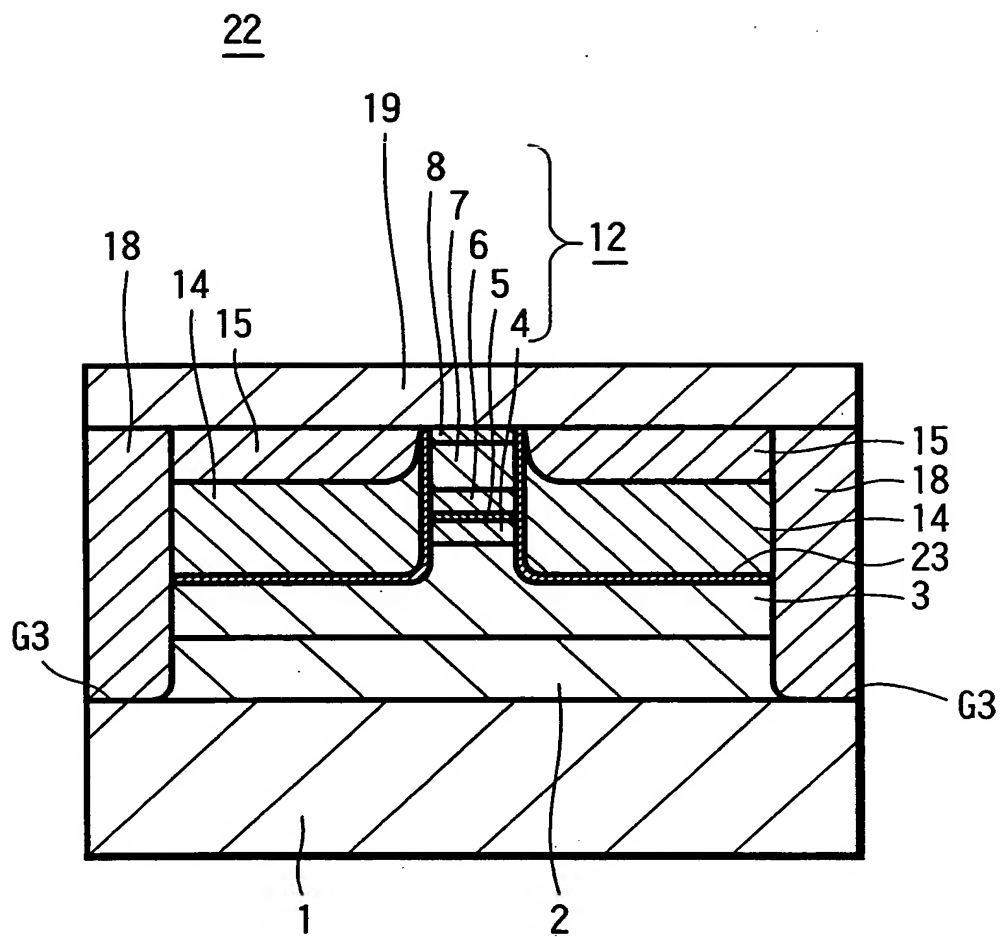


FIG. 21

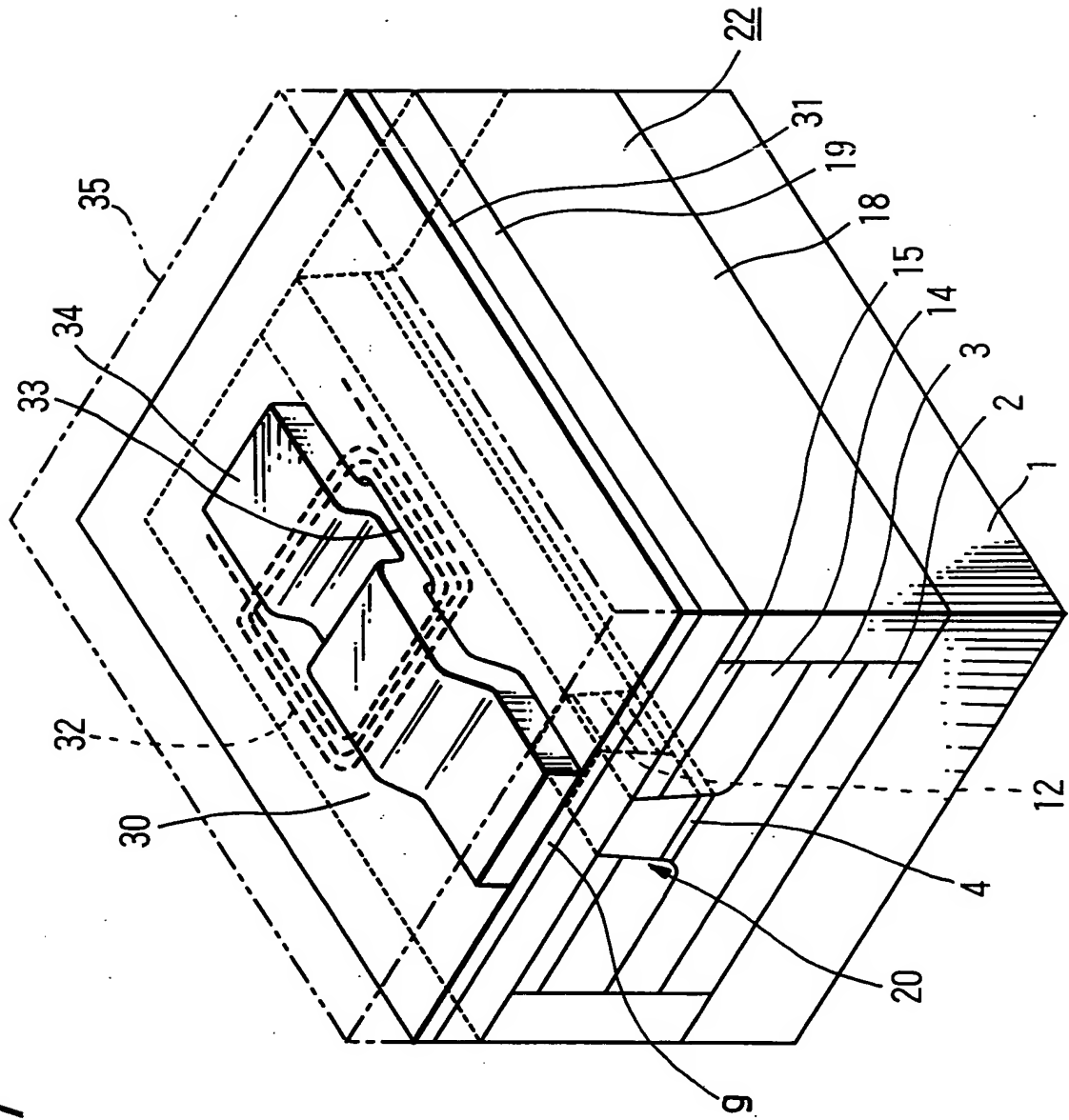


FIG. 22

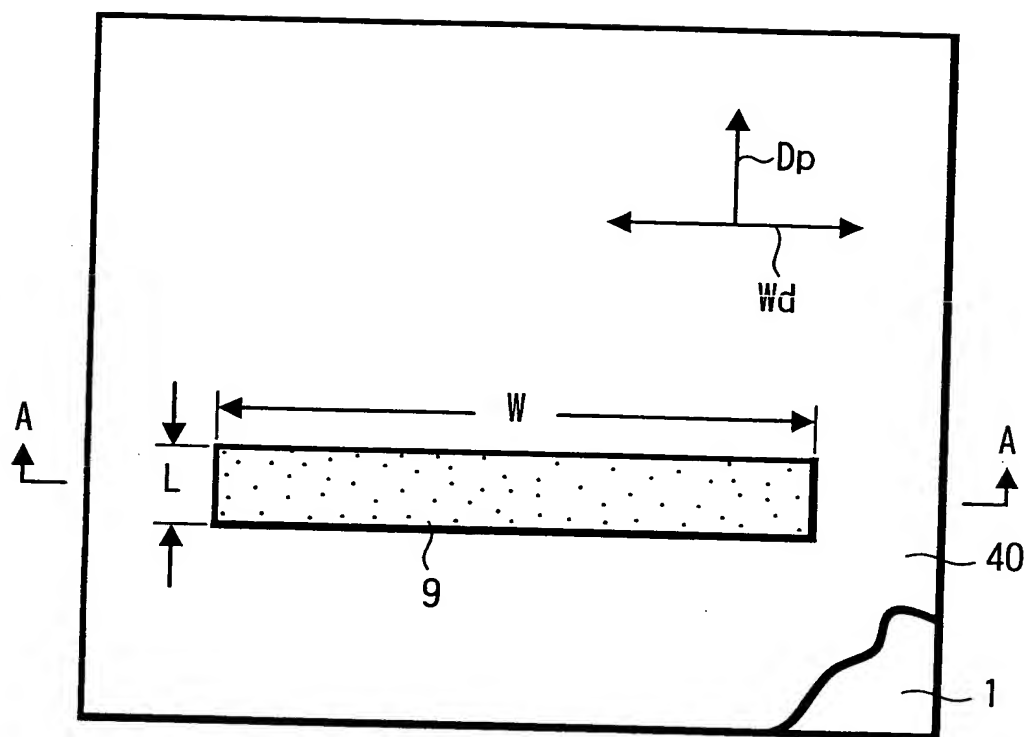


FIG. 23

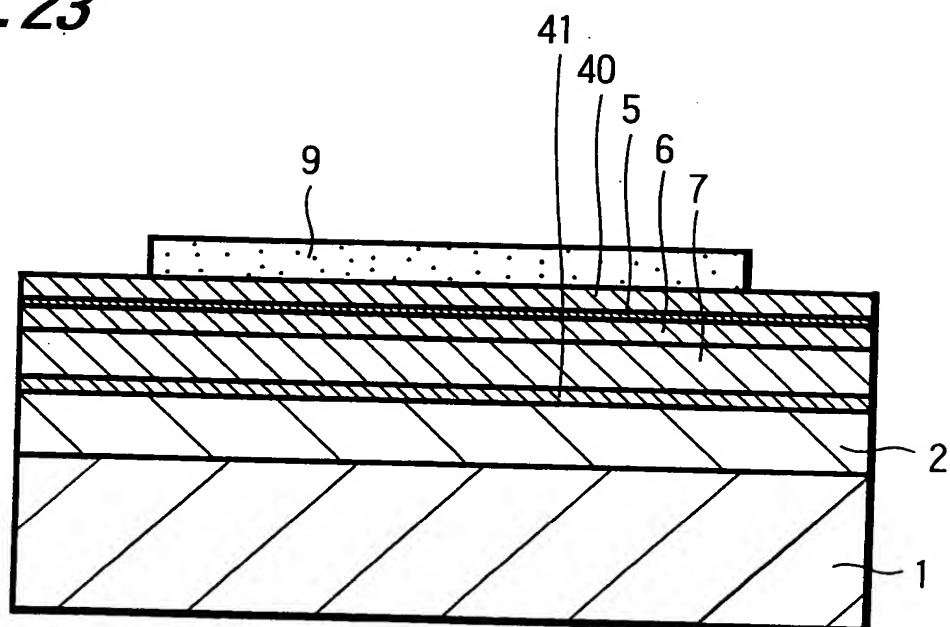


FIG. 24

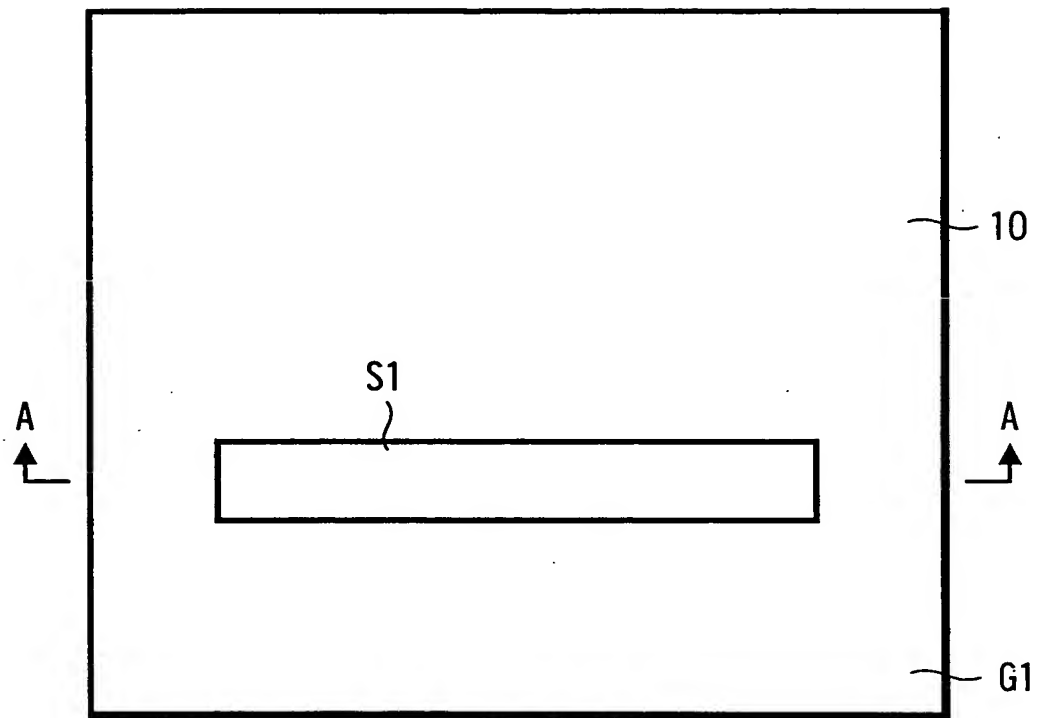


FIG. 25

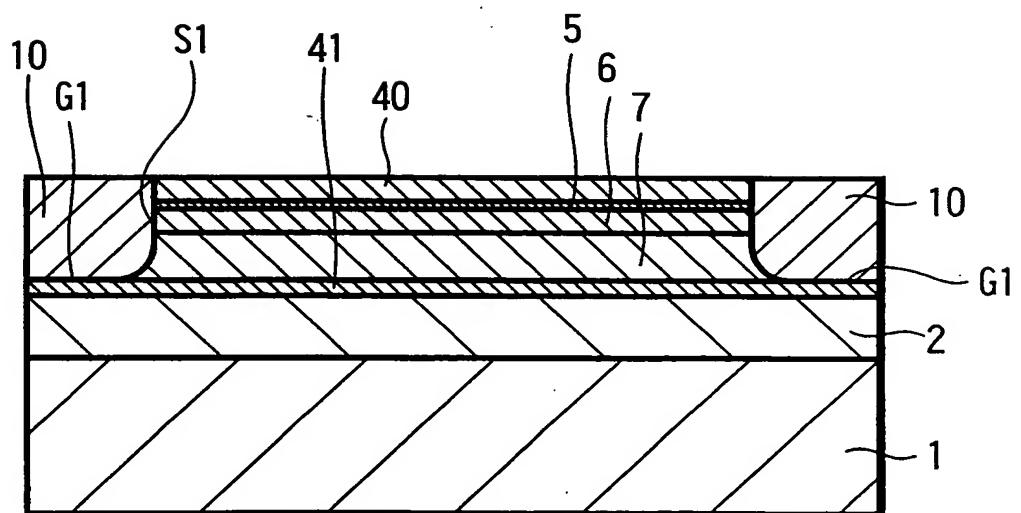


FIG. 26

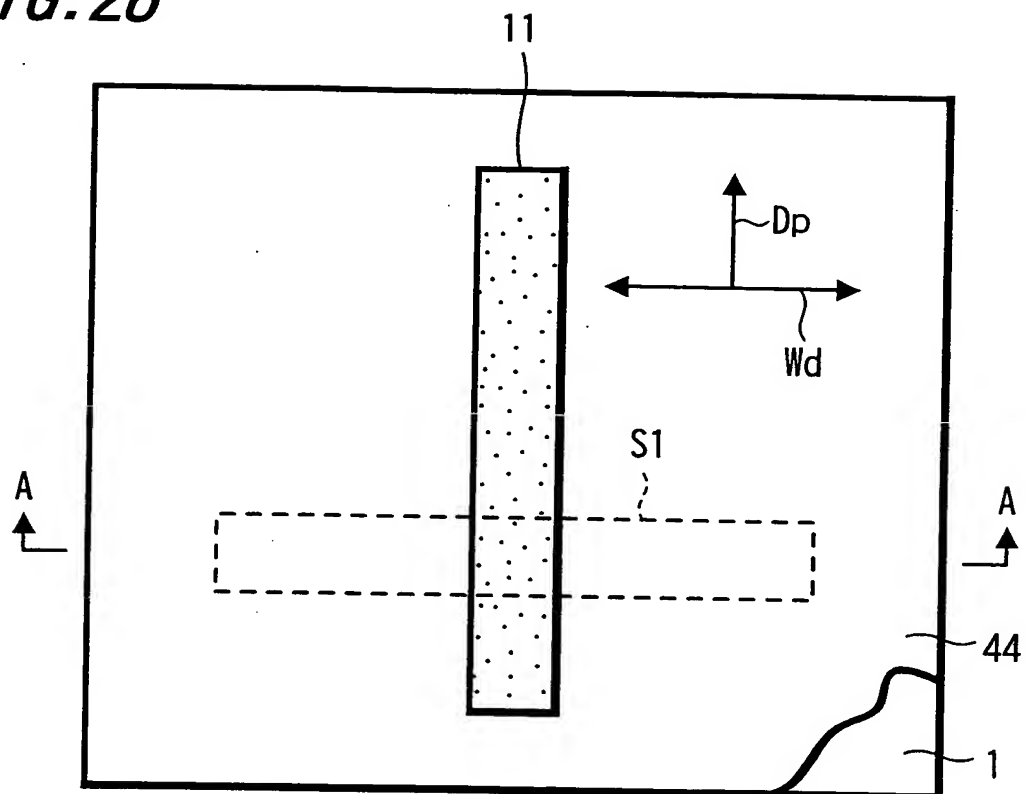


FIG. 27

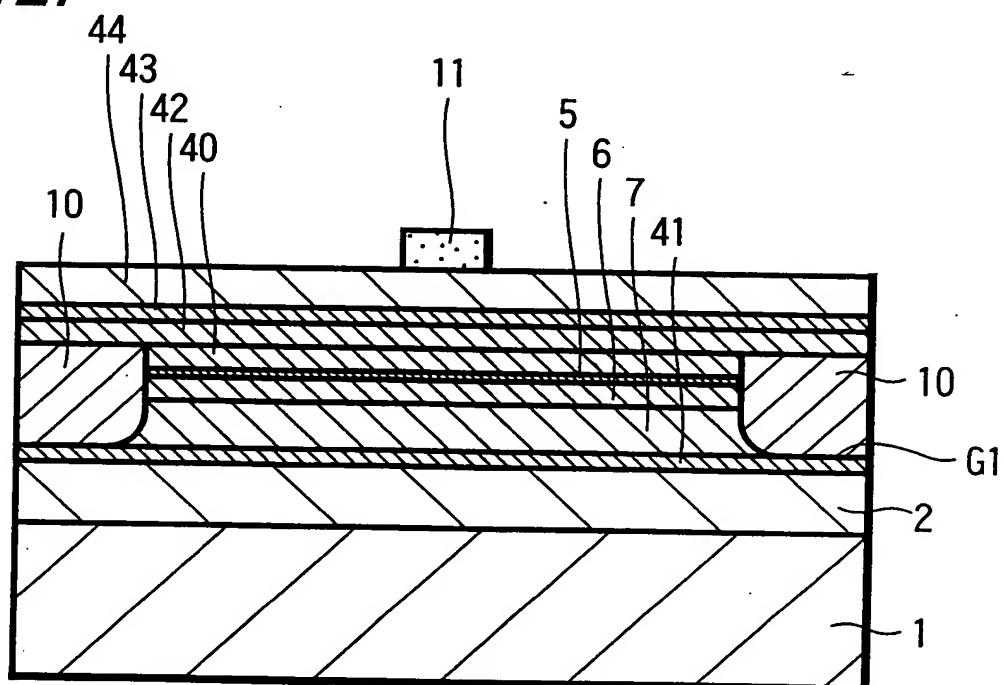


FIG. 28

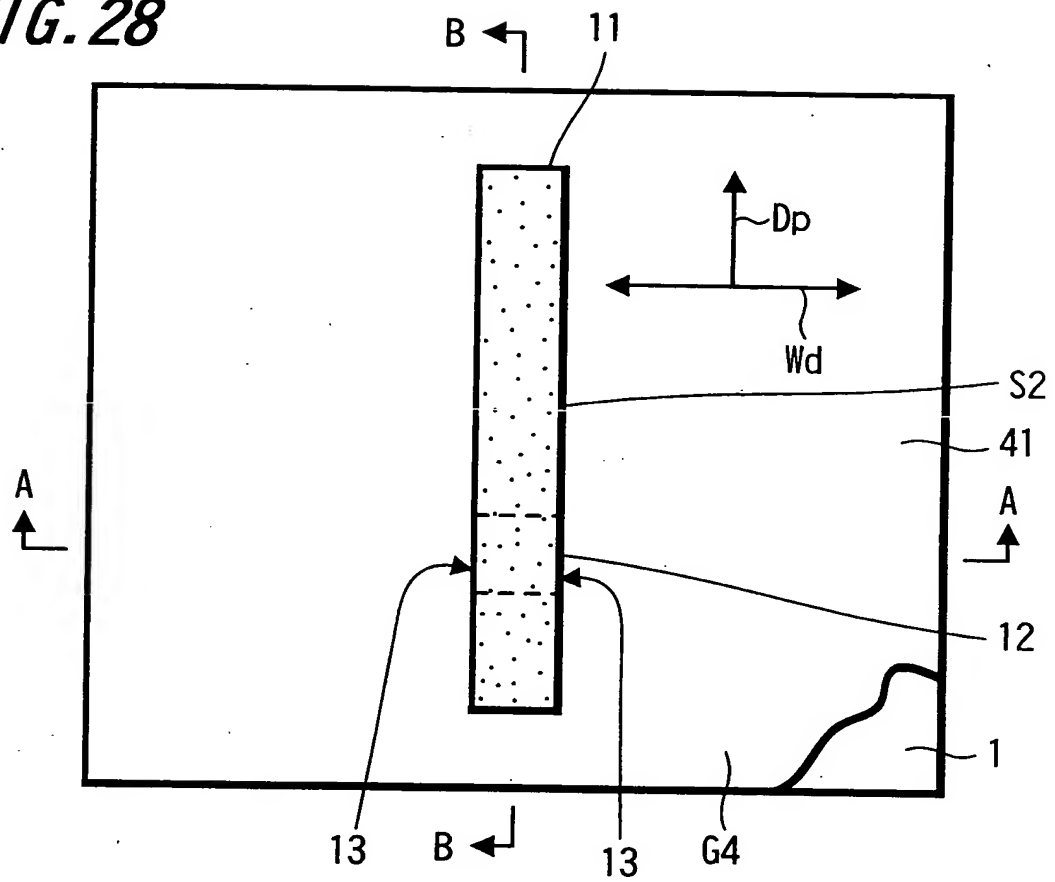


FIG. 29

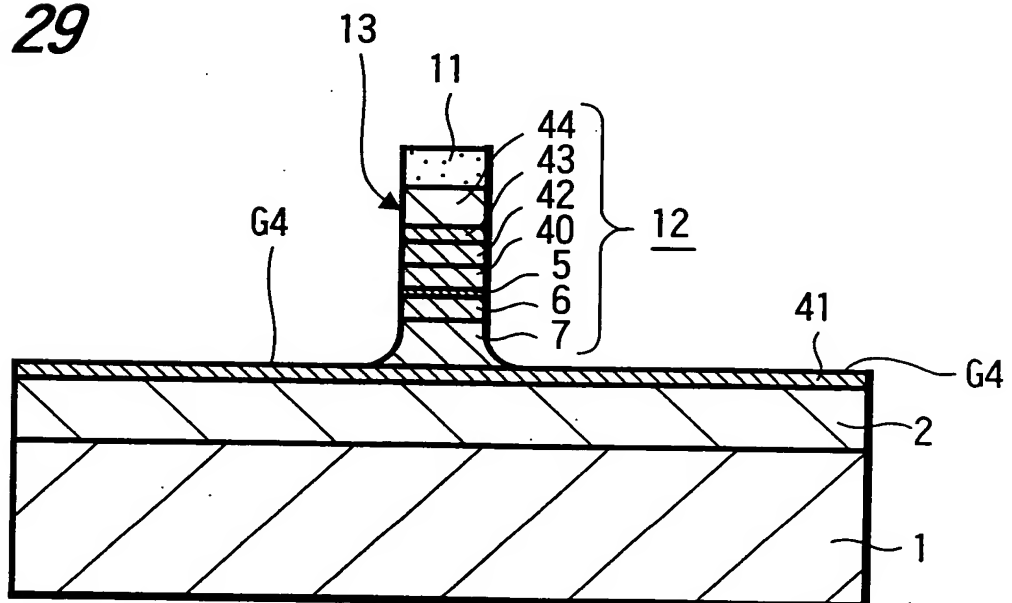


FIG. 30

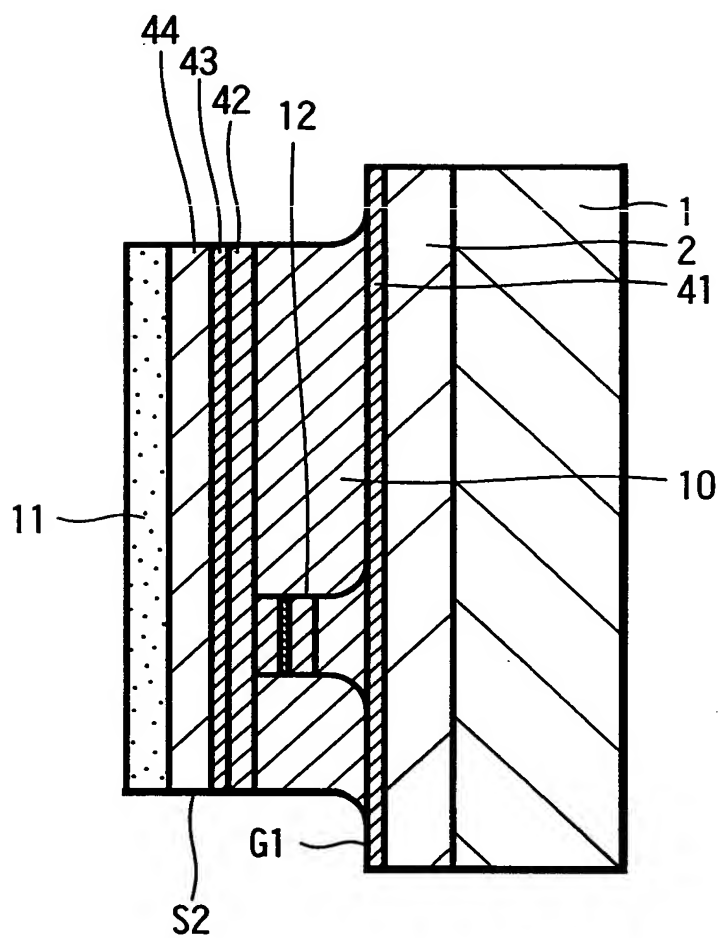


FIG. 31

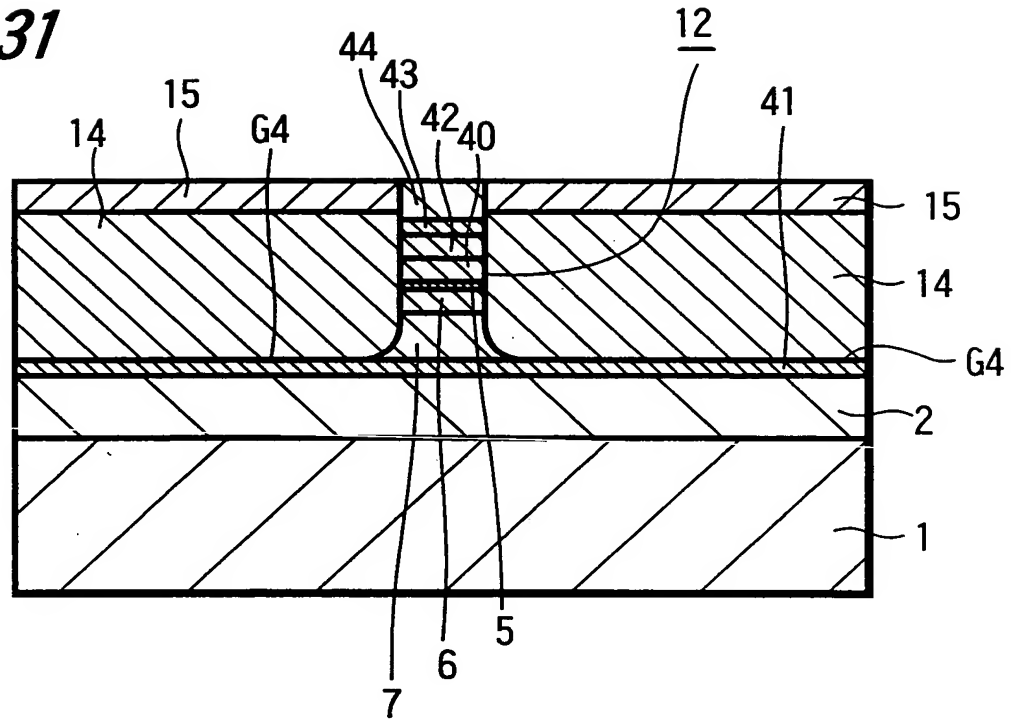


FIG. 32

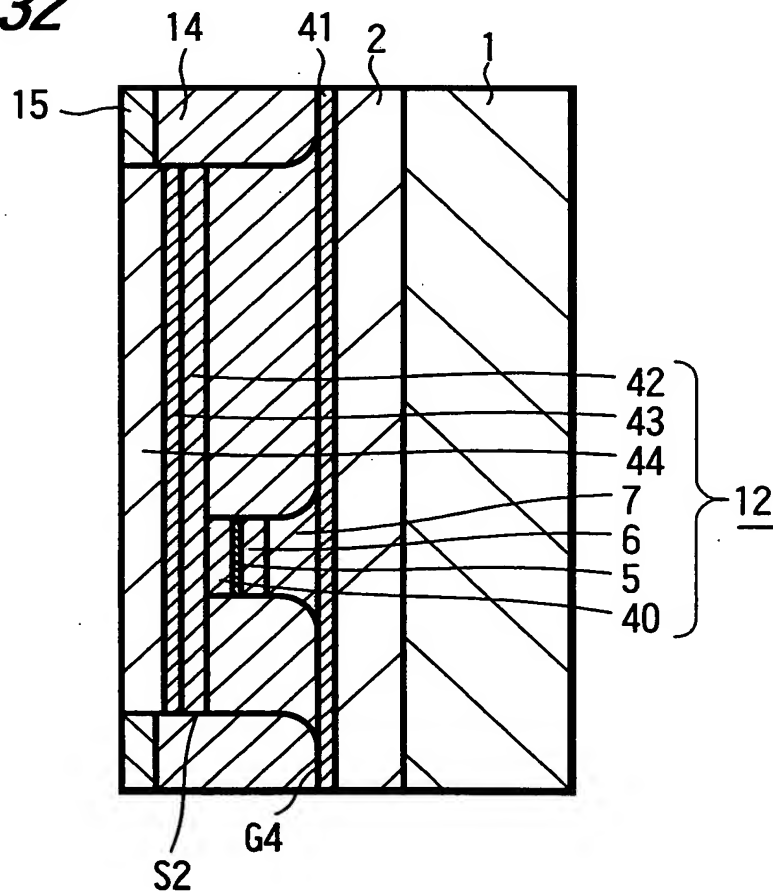


FIG. 33

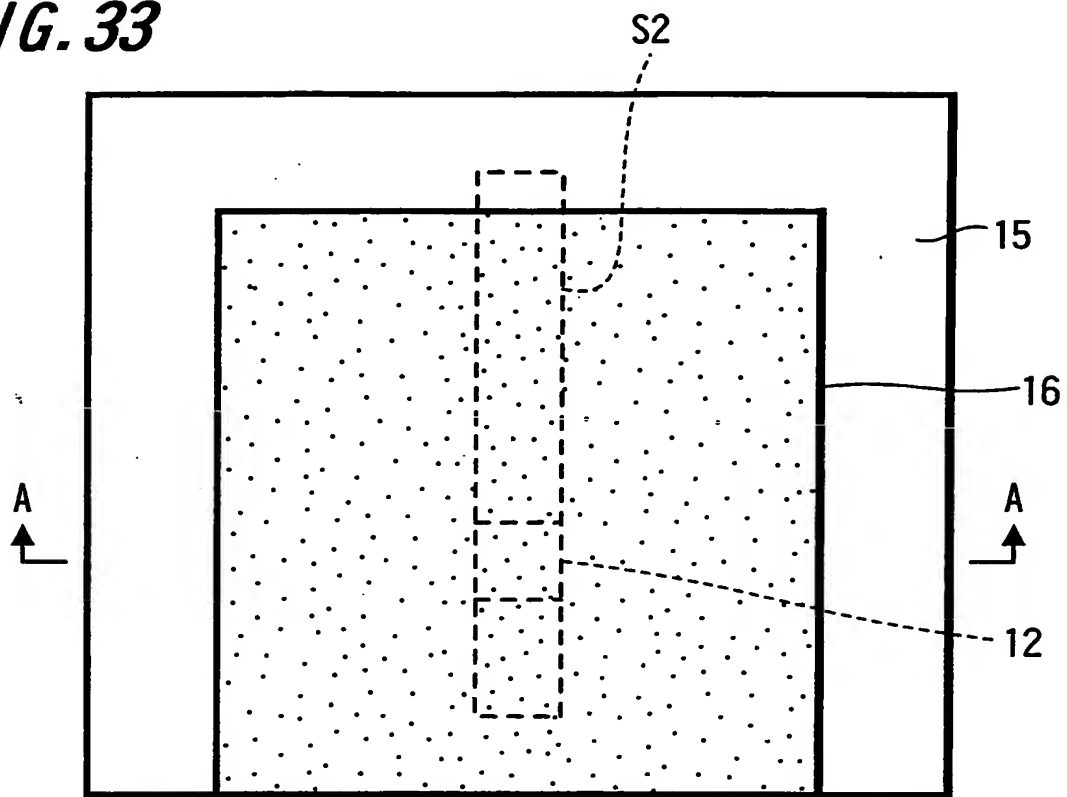


FIG. 34

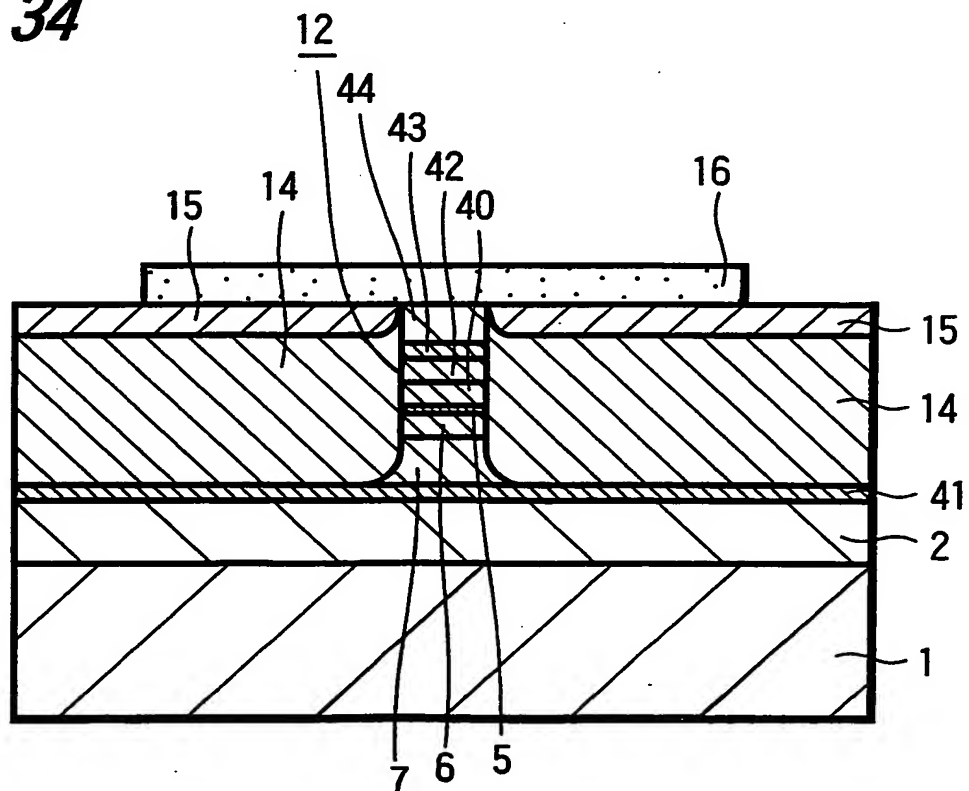


FIG. 35

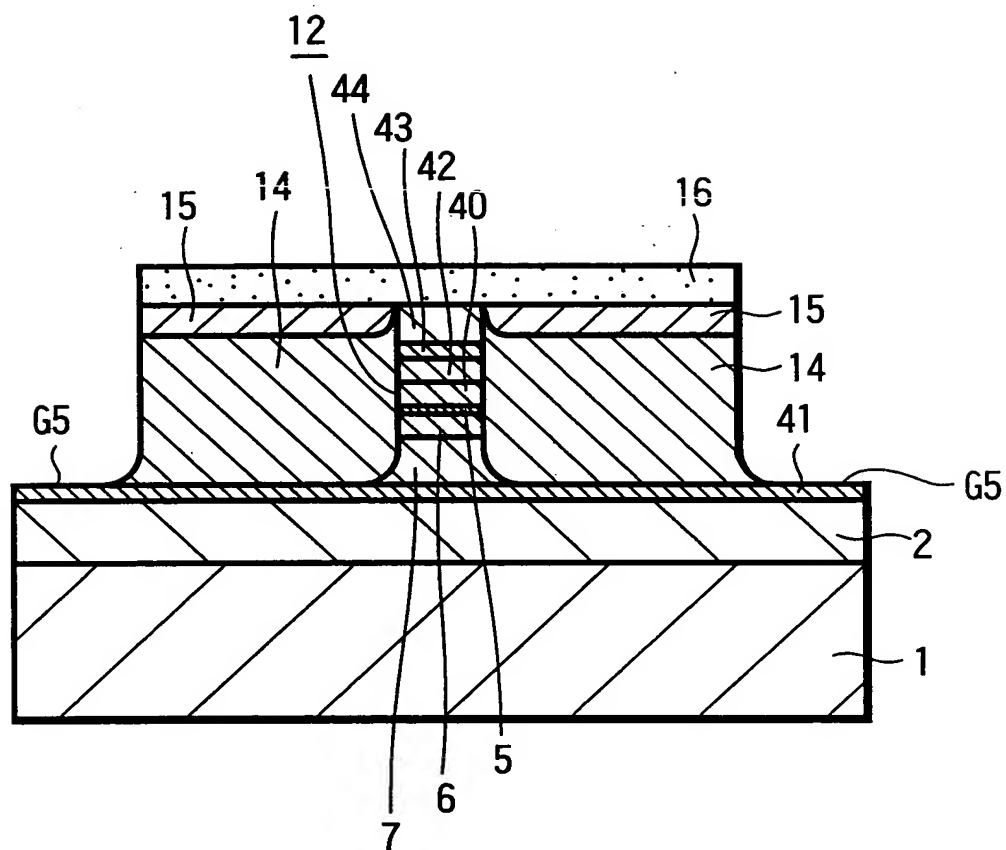


FIG. 36

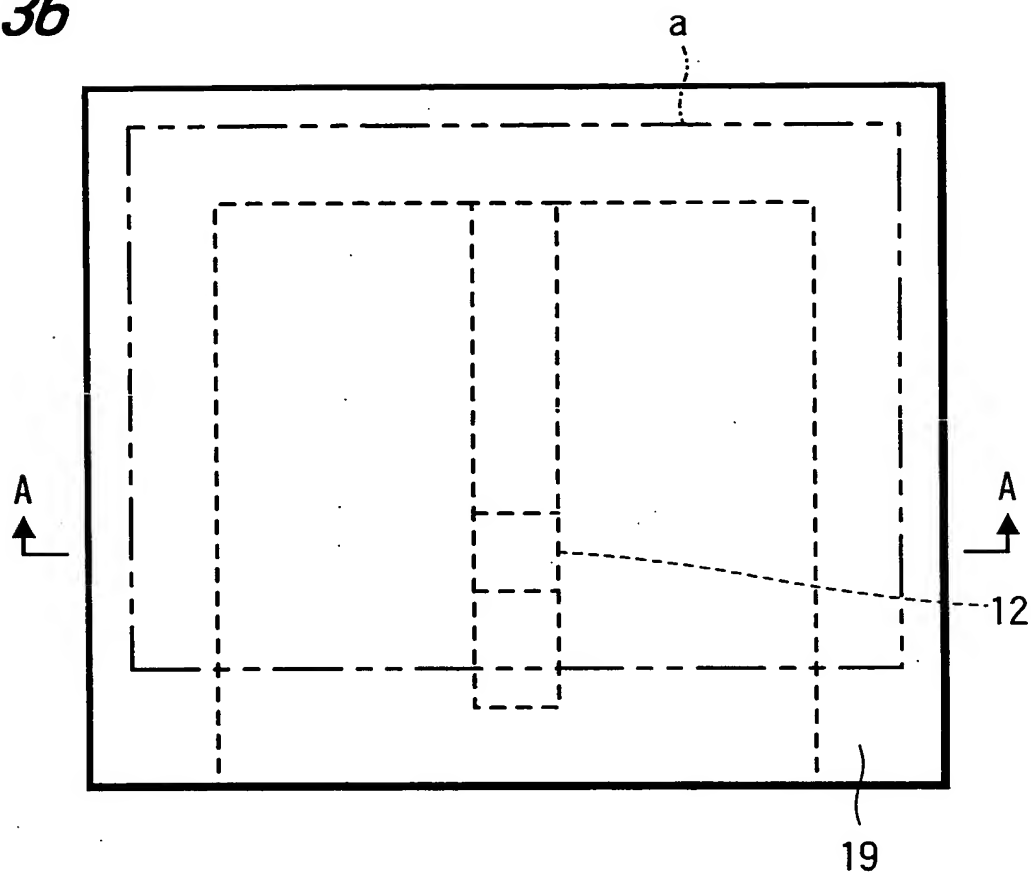


FIG. 37

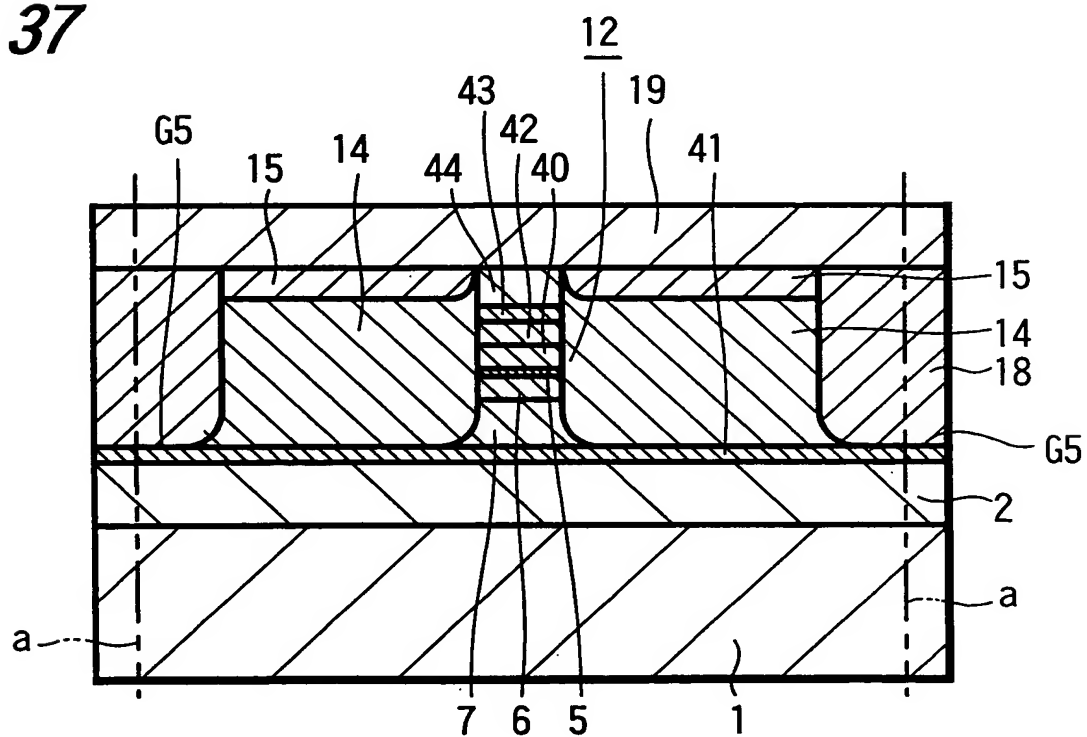


FIG. 40

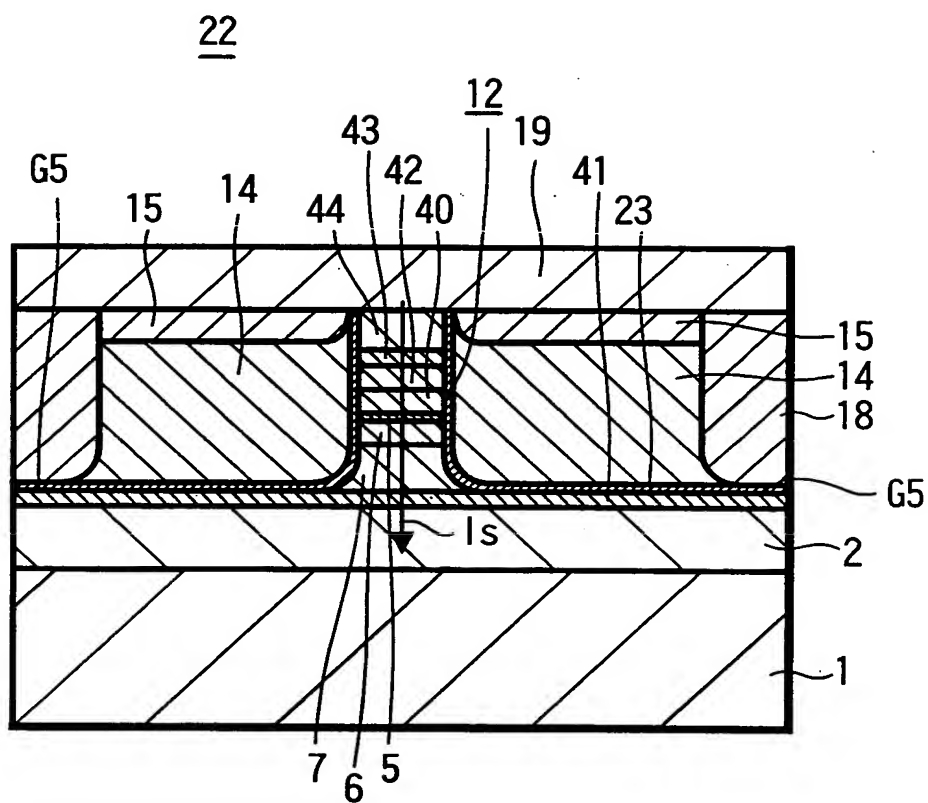


FIG. 41

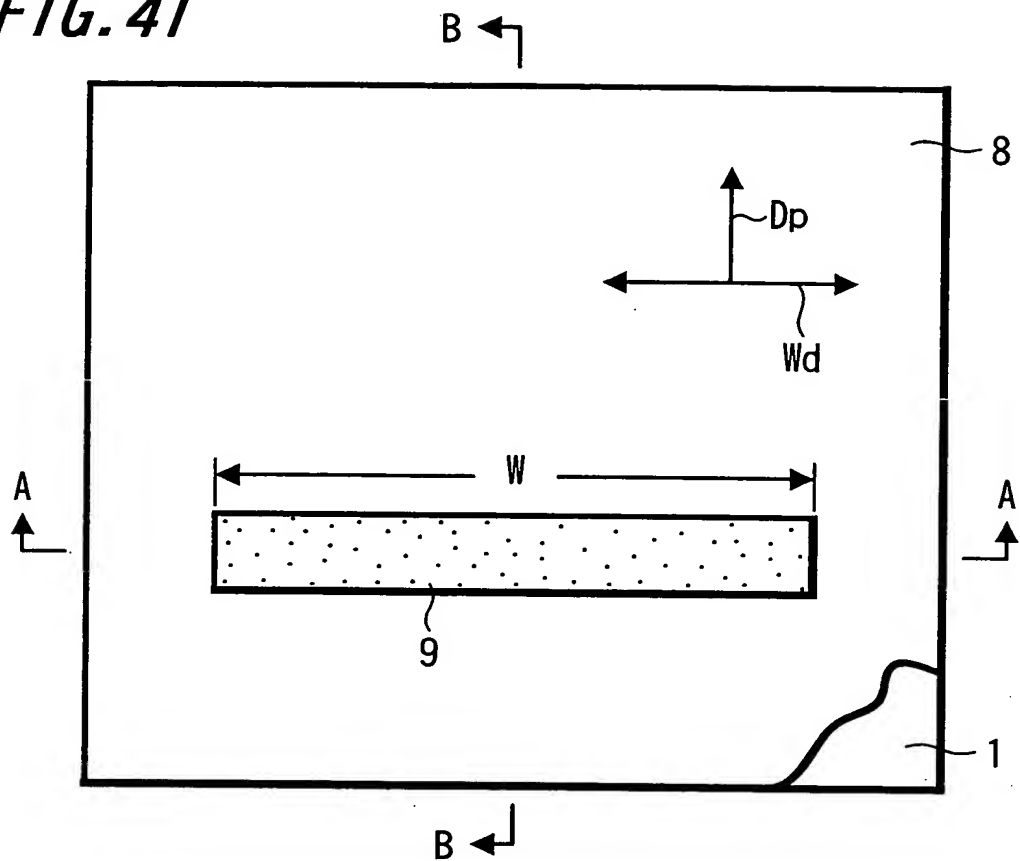


FIG. 42

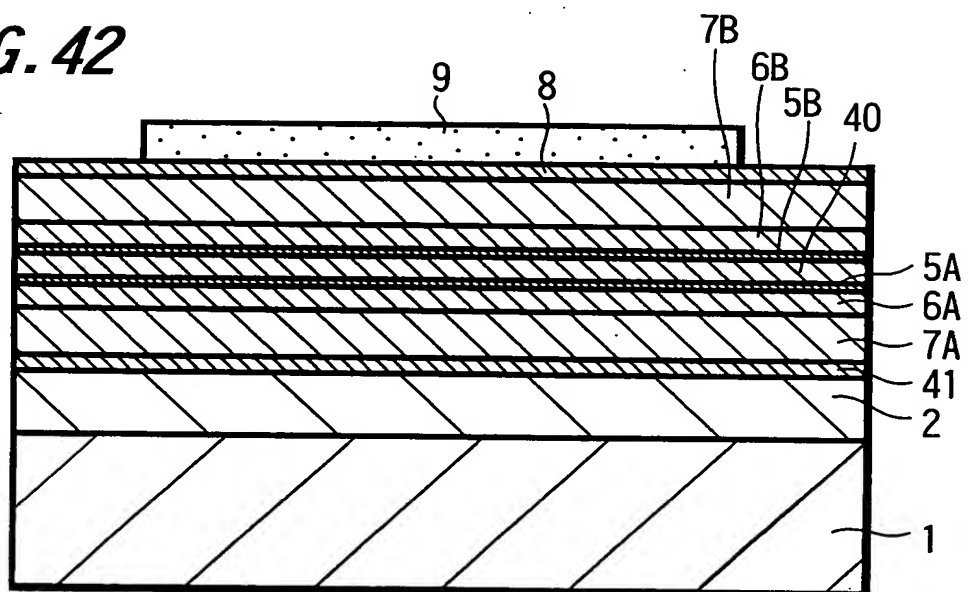


FIG. 45

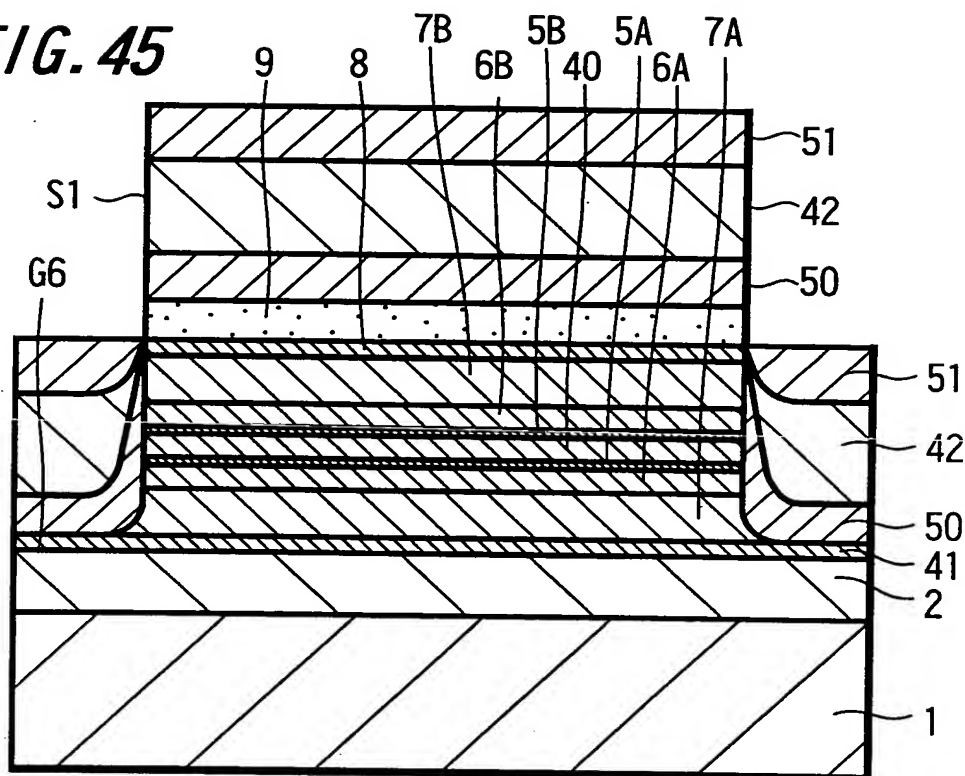


FIG. 46

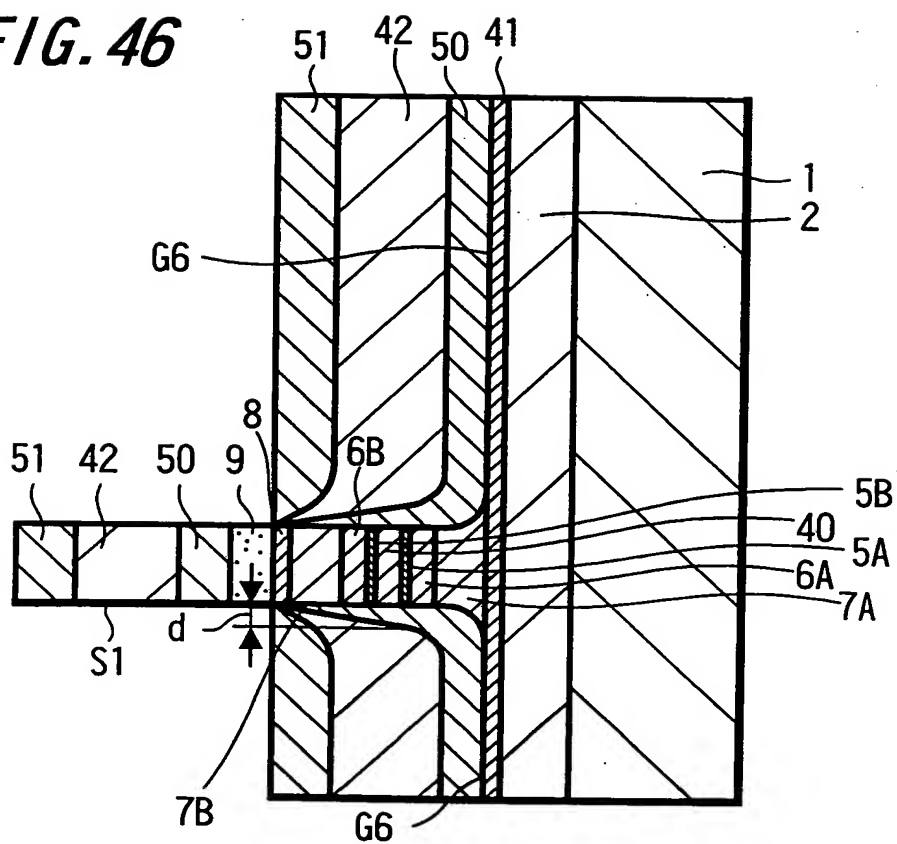


FIG. 47

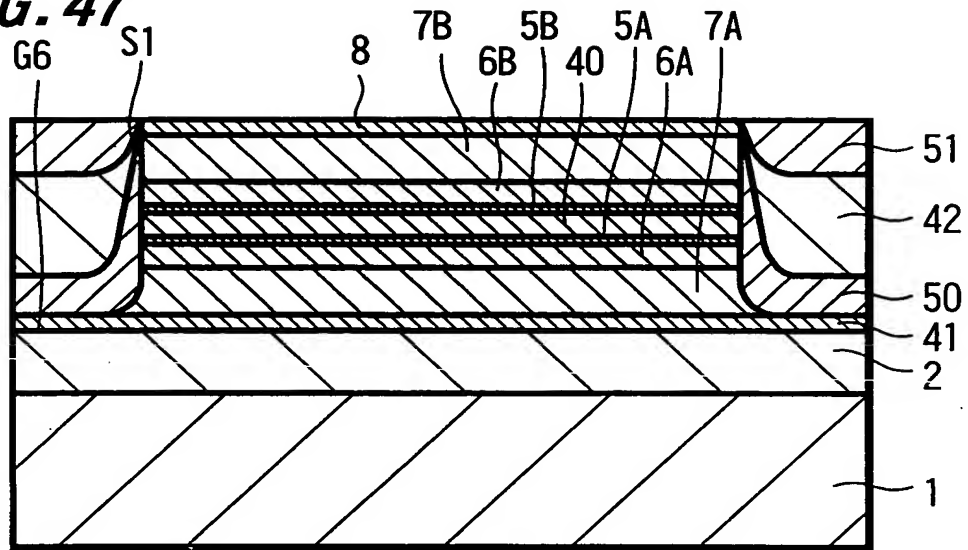


FIG. 48

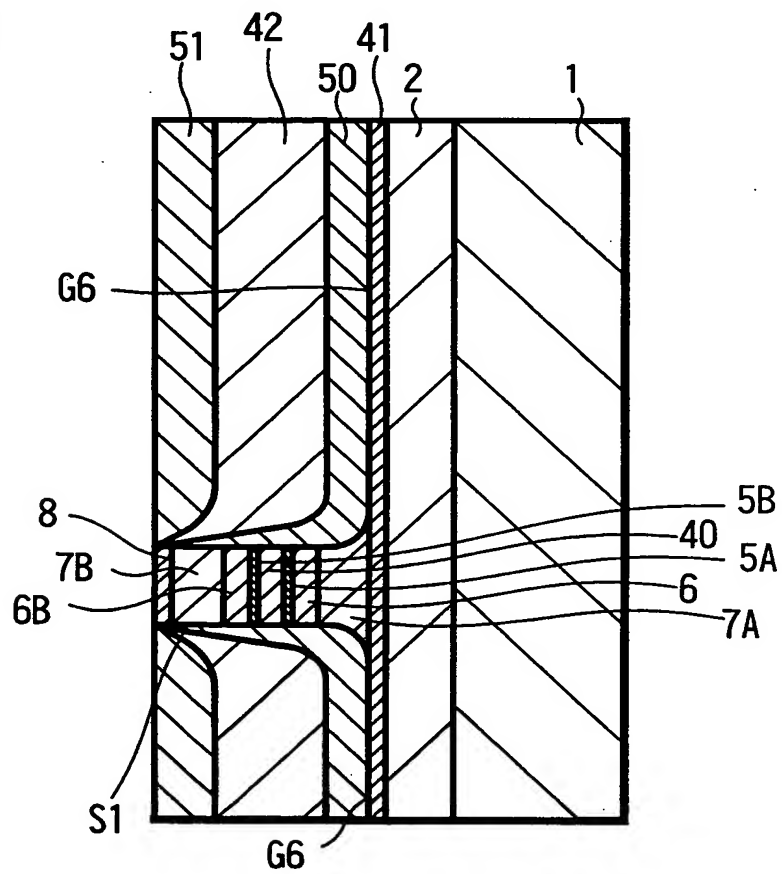


FIG. 49

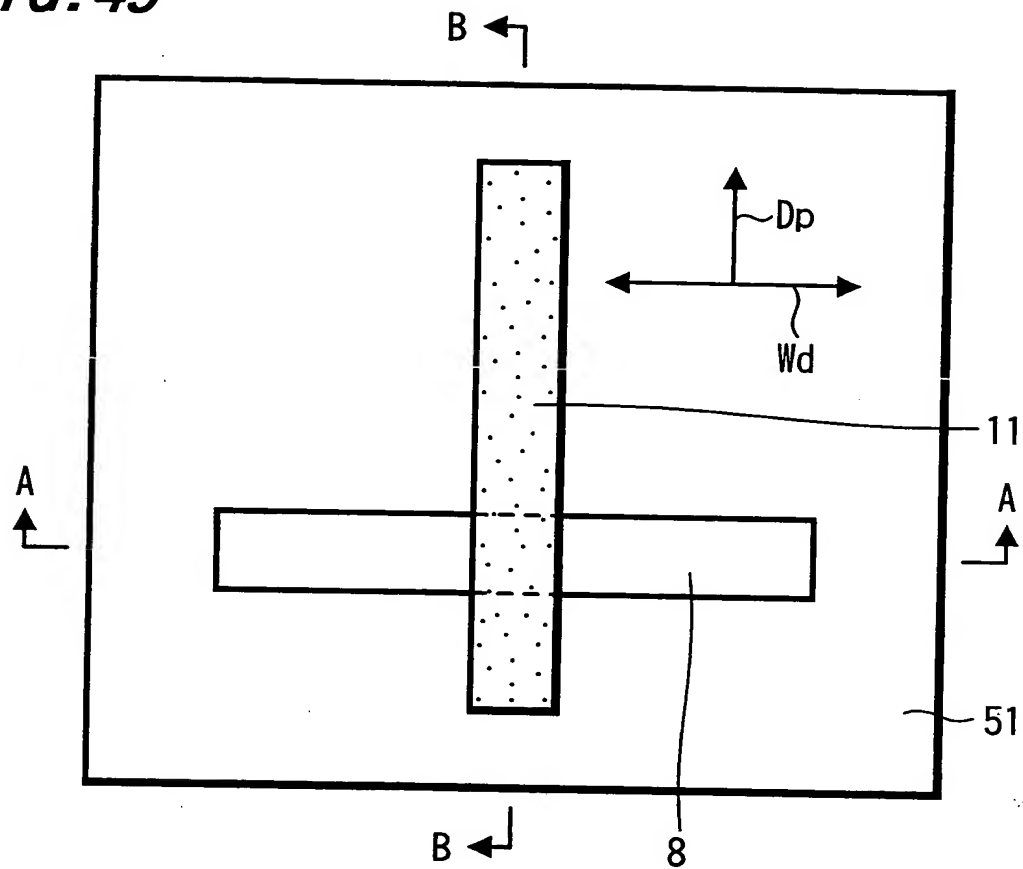


FIG. 50

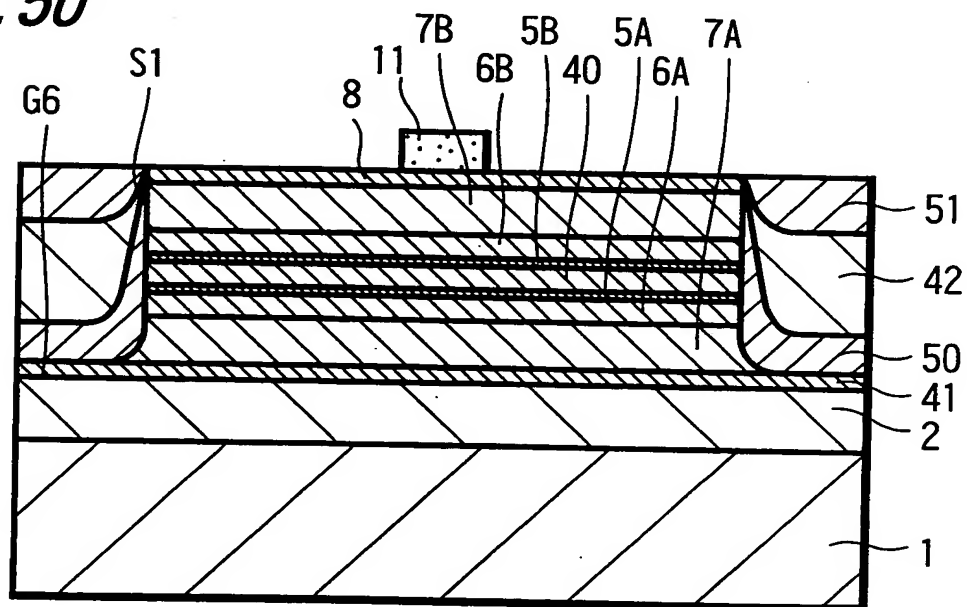


FIG. 51

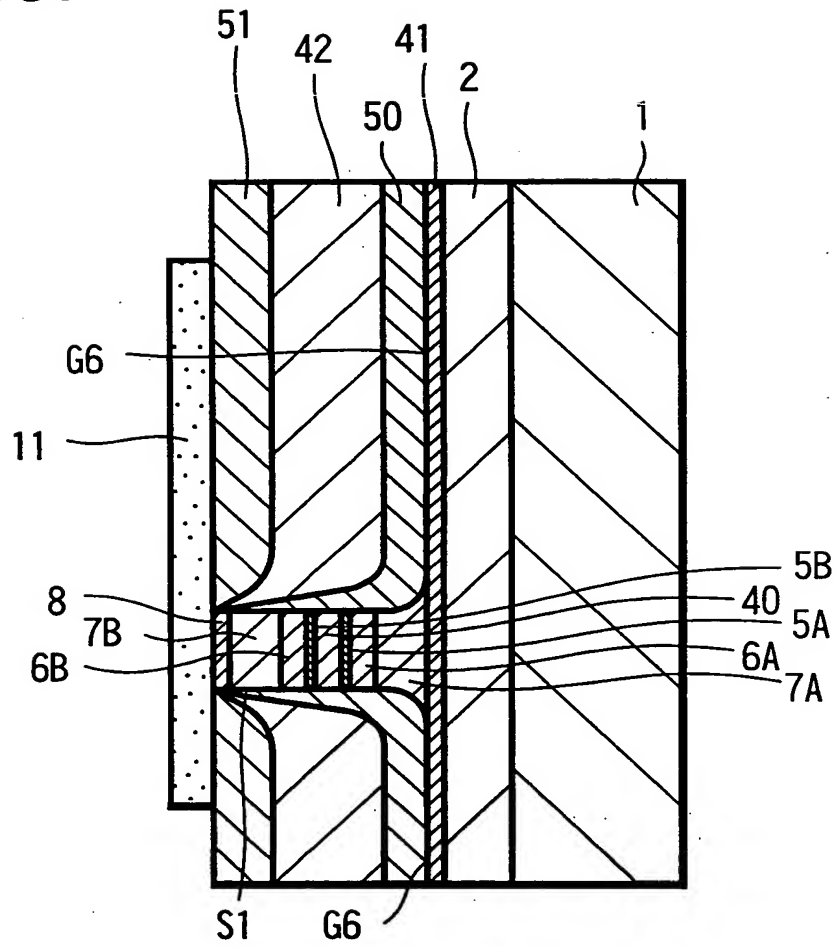


FIG. 52

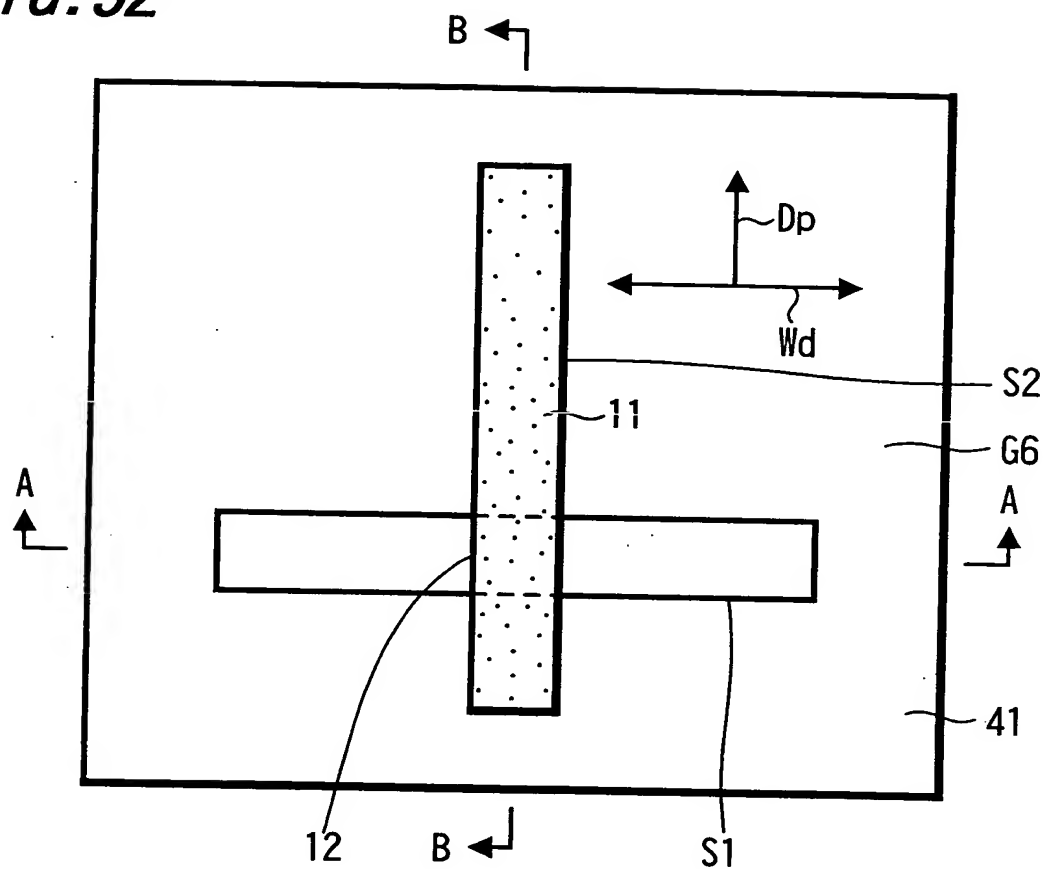


FIG. 53

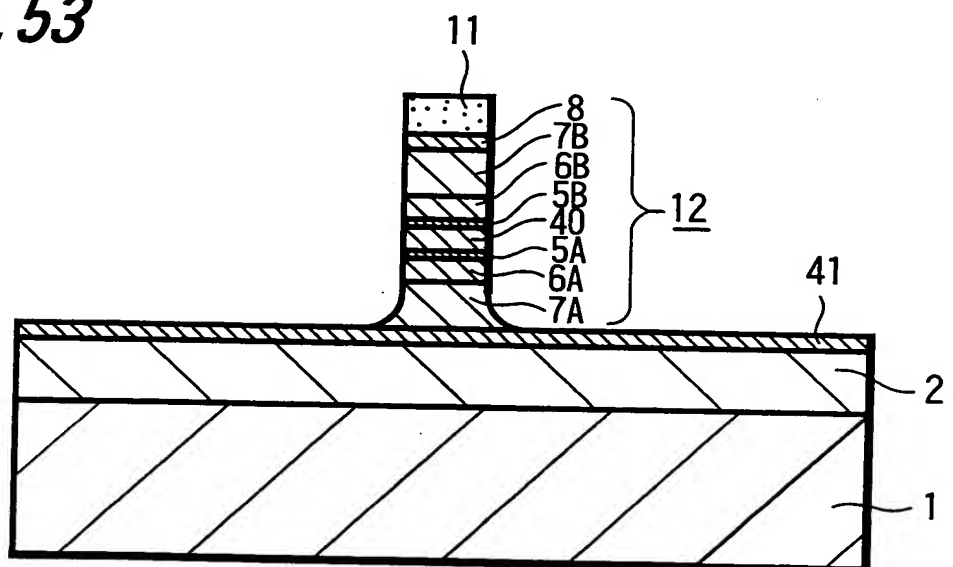


FIG. 54

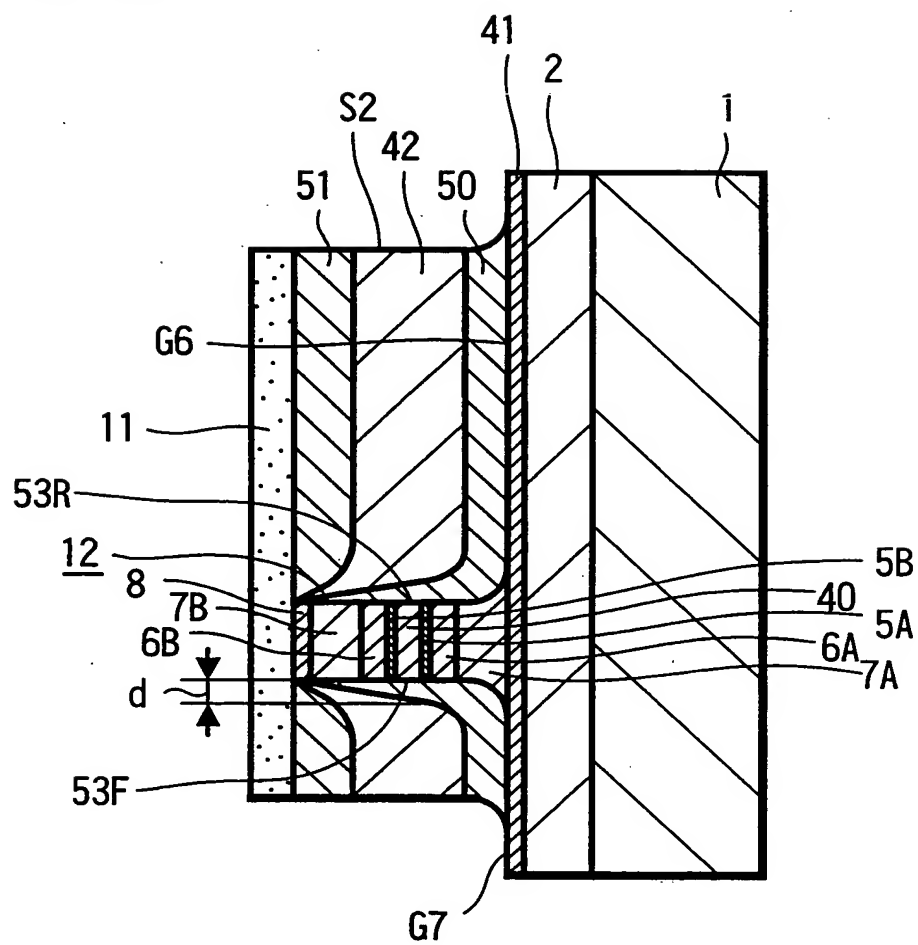


FIG. 55

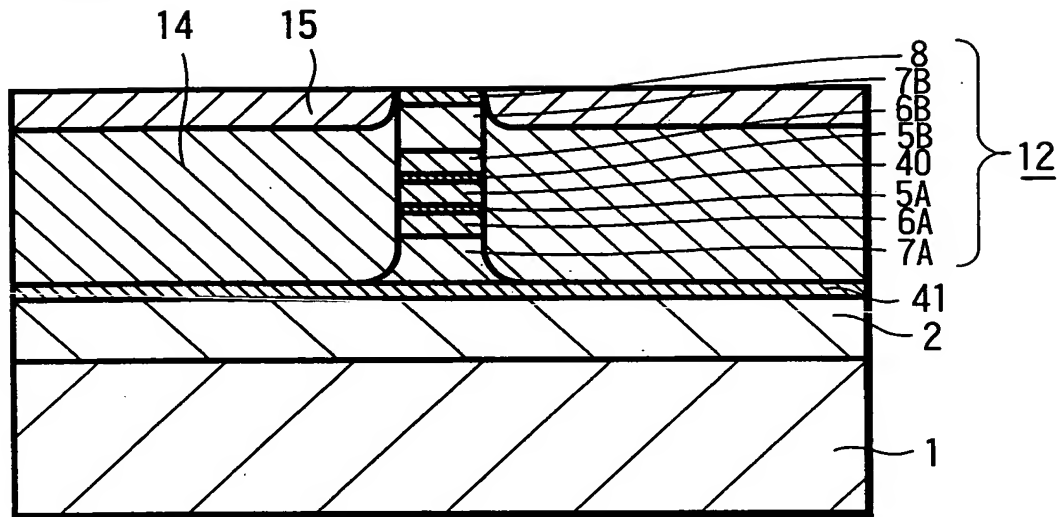


FIG. 56

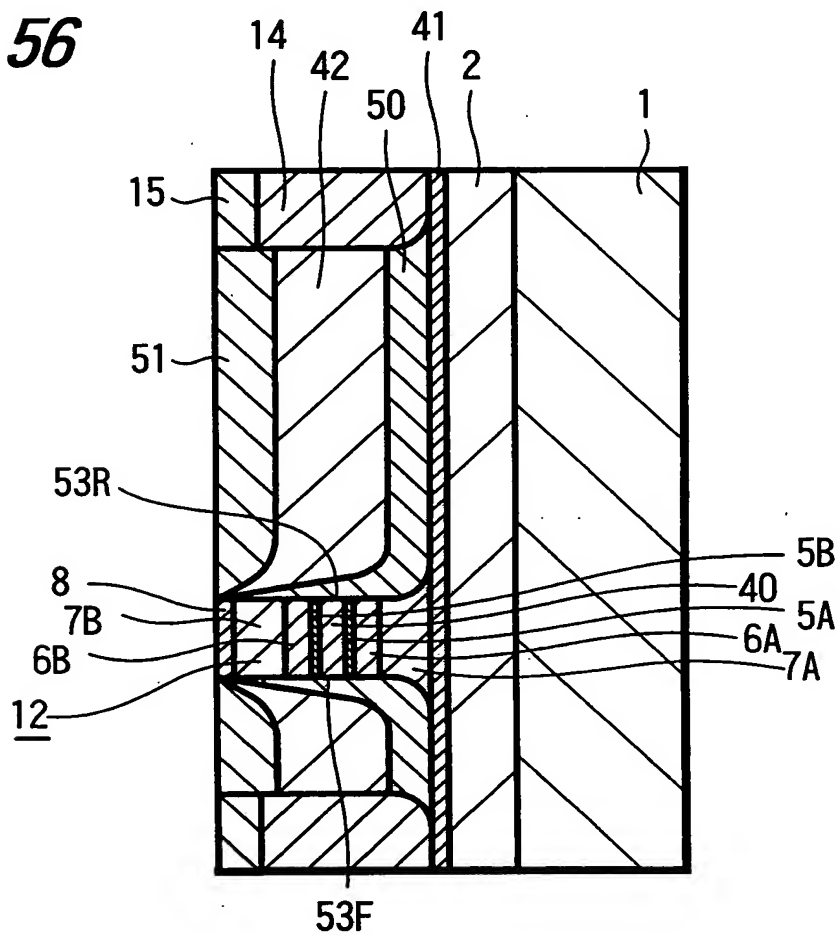


FIG. 57

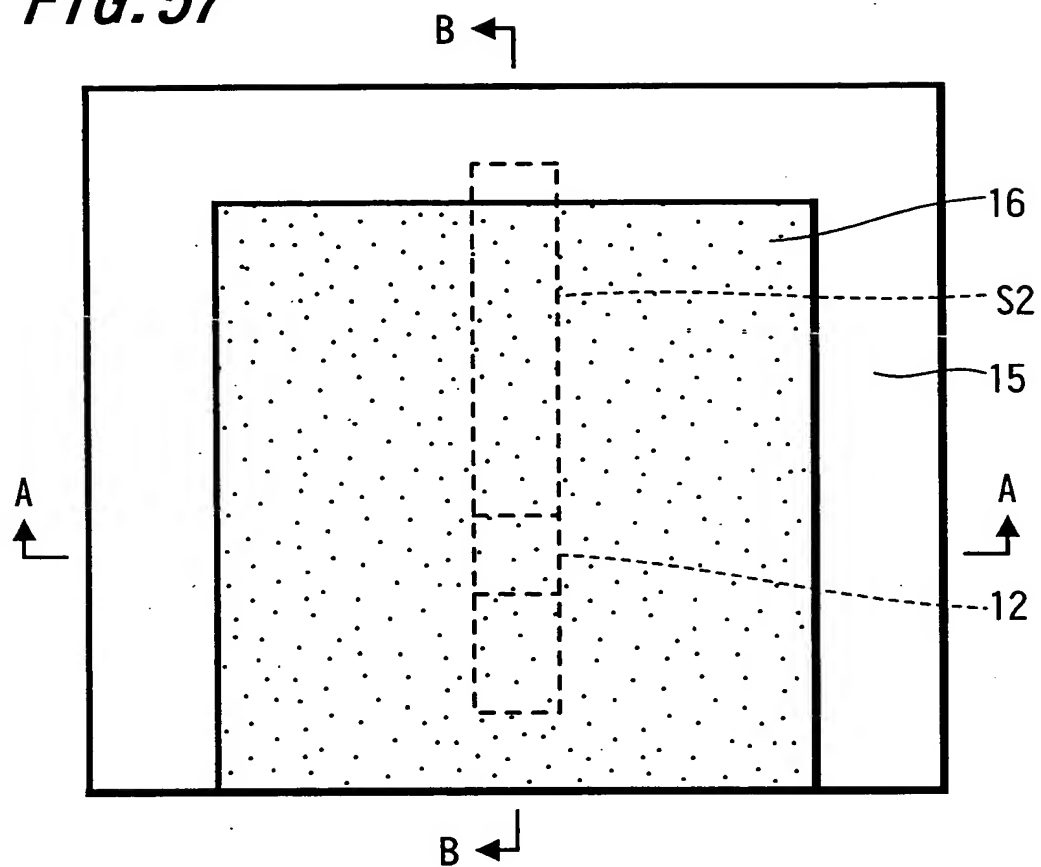


FIG. 58

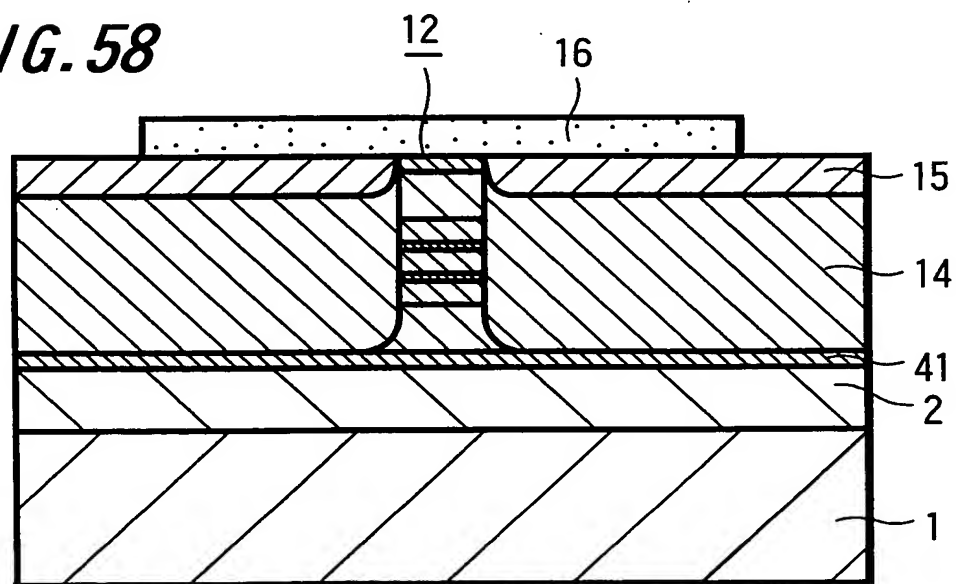


FIG. 59

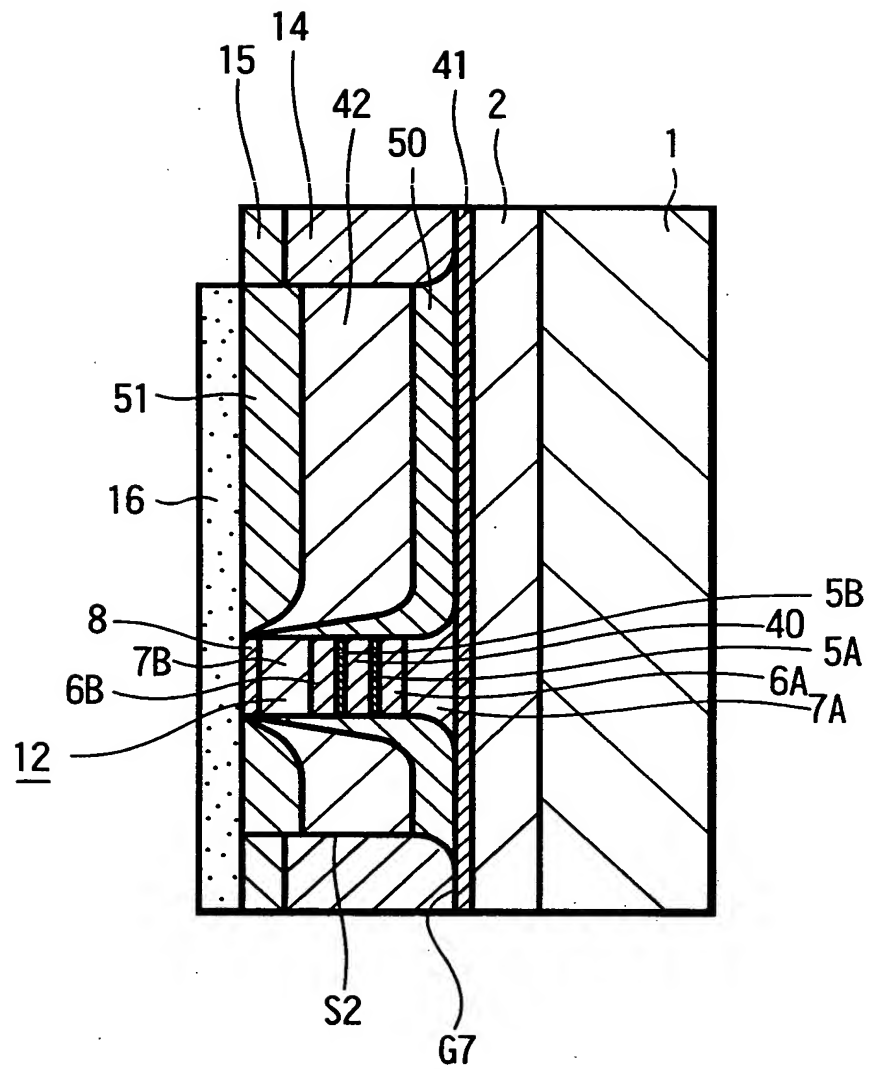


FIG. 60

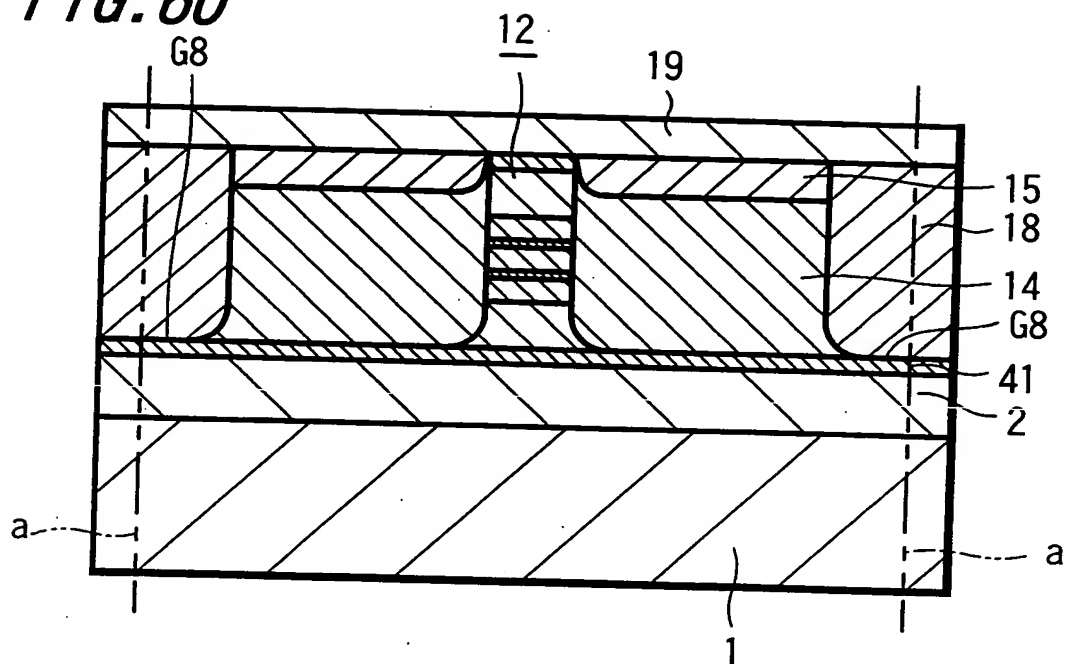


FIG. 61

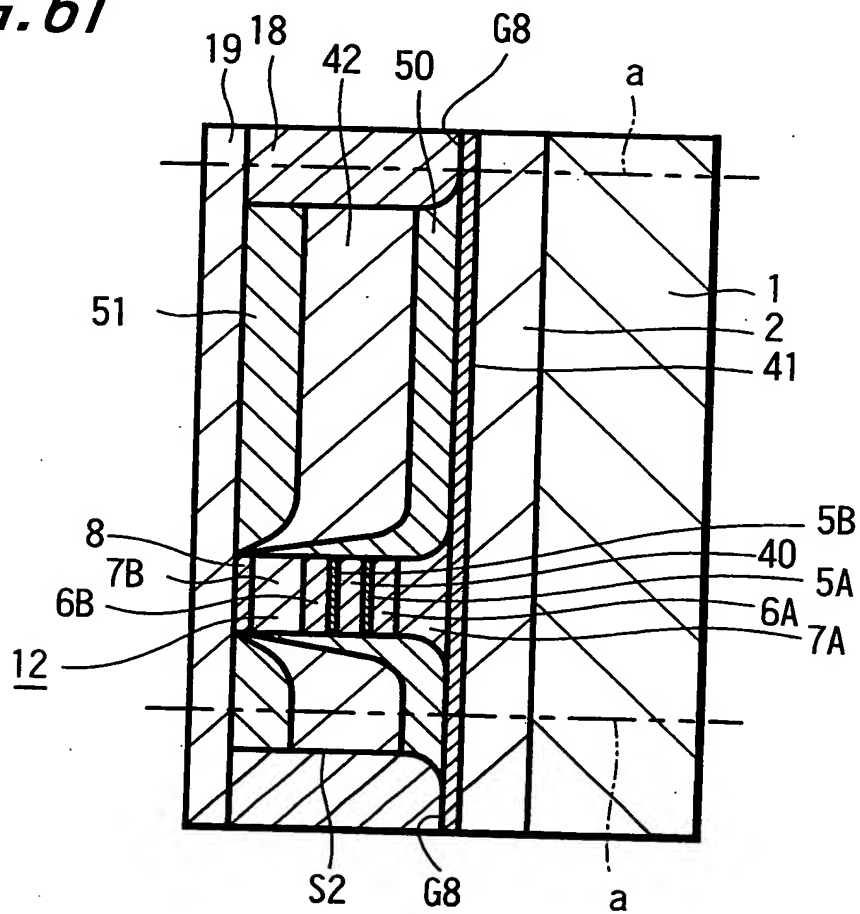


FIG. 62

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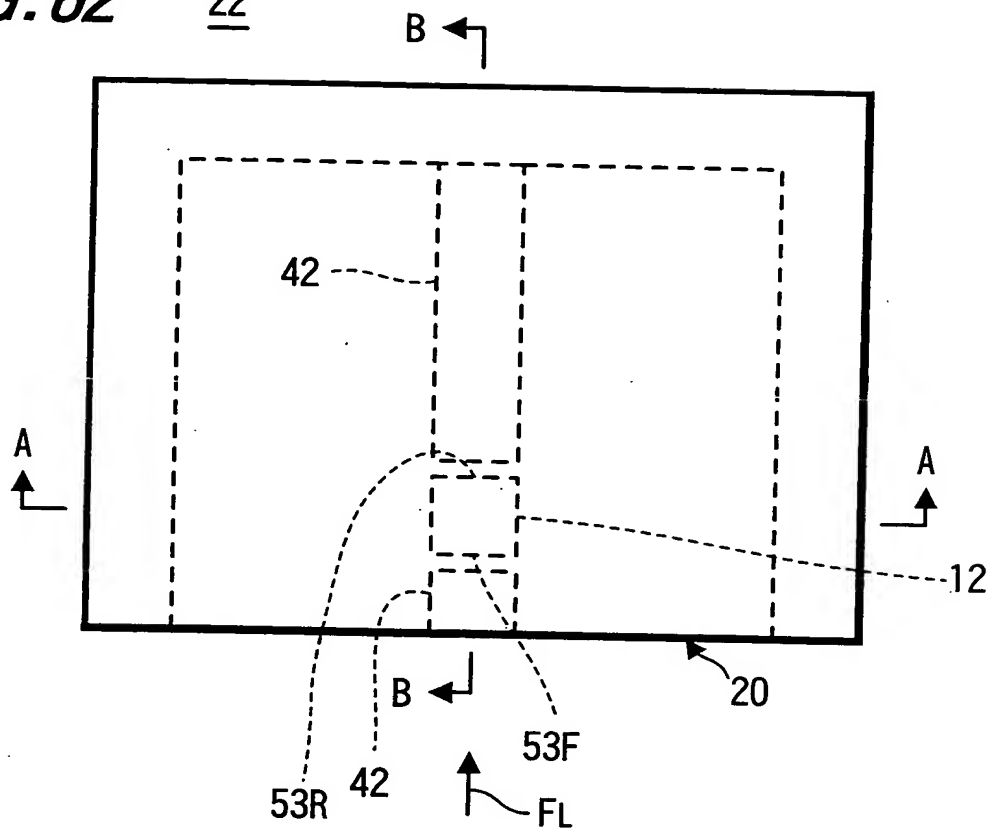


FIG. 63

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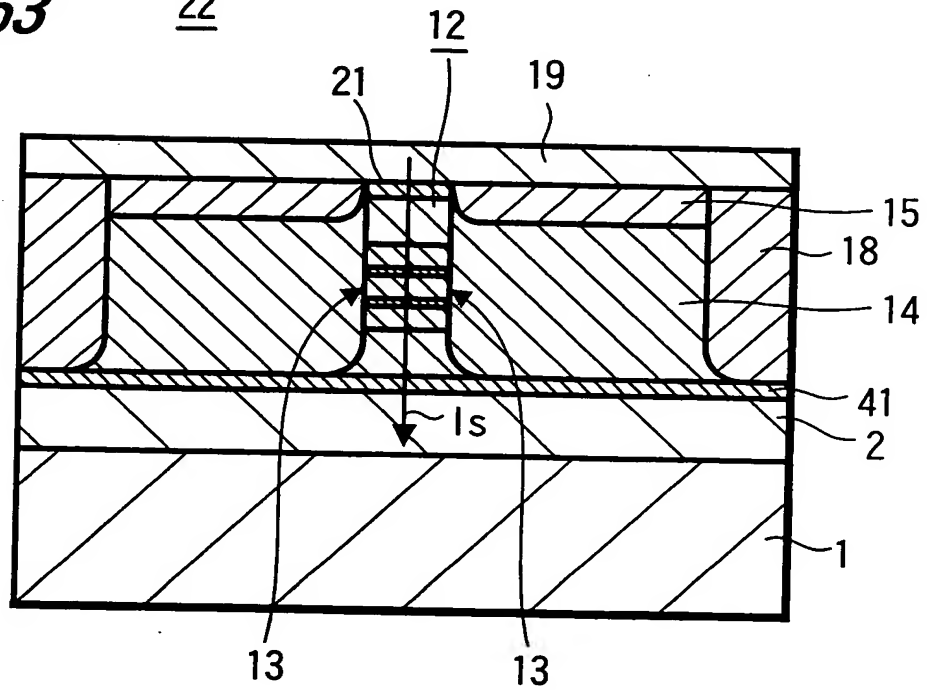


FIG. 65

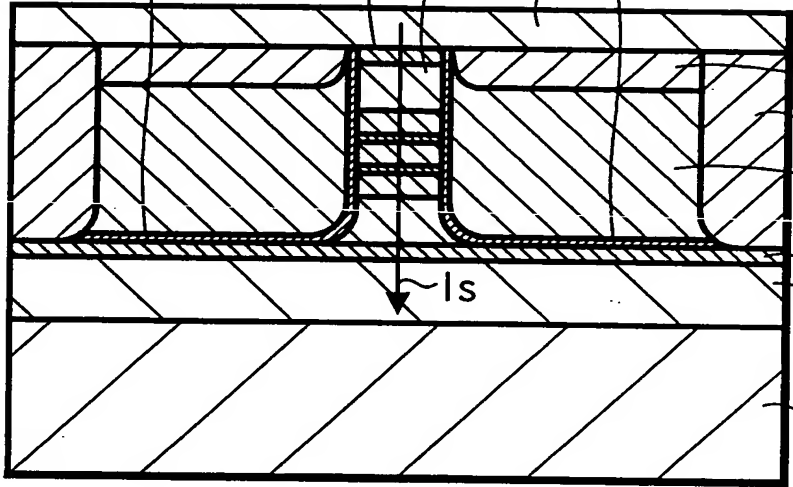
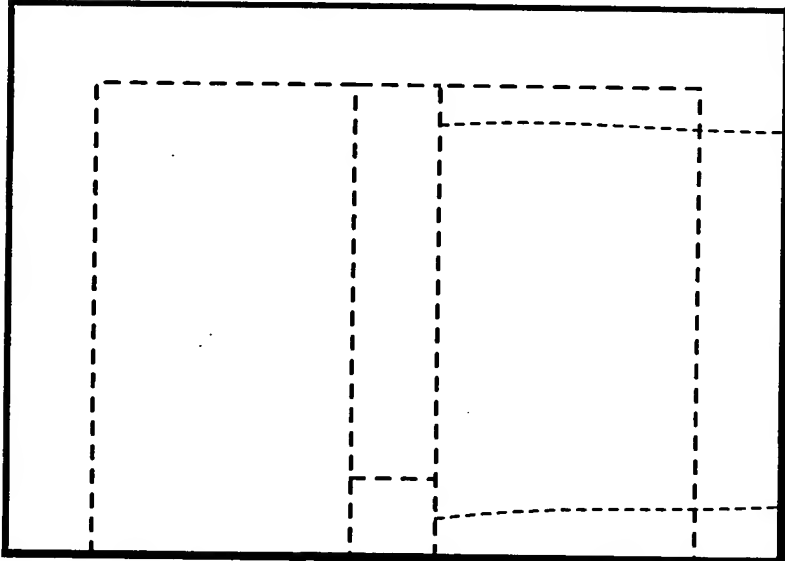


FIG. 66



-S2

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